

# FDP7045L/FDB7045L

## N-Channel Logic Level PowerTrench® MOSFET

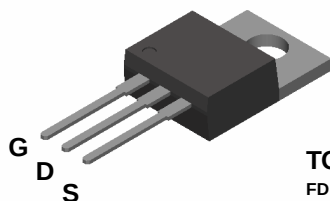
### General Description

This N-Channel Logic Level MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

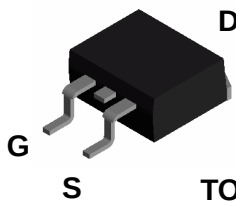
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable  $R_{DS(on)}$  specifications resulting in DC/DC power supply designs with higher overall efficiency.

### Features

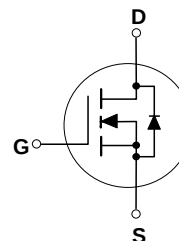
- 100 A, 30 V.  $R_{DS(on)} = 0.0045 \Omega @ V_{GS} = 10 \text{ V}$   
 $R_{DS(on)} = 0.006 \Omega @ V_{GS} = 4.5 \text{ V}$ .
- Critical DC electrical parameters specified at elevated temperature.
- Rugged internal source-drain diode can eliminate the need for an external Zener diode transient suppressor.
- High performance PowerTrench technology for extremely low  $R_{DS(on)}$ .
- 175°C maximum junction temperature rating.



**TO-220**  
FDP Series



**TO-263AB**  
FDB Series



### Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

| Symbol         | Parameter                                          | FDP7045L    | FDB7045L | Units               |
|----------------|----------------------------------------------------|-------------|----------|---------------------|
| $V_{DSS}$      | Drain-Source Voltage                               | 30          |          | V                   |
| $V_{GSS}$      | Gate-Source Voltage                                | $\pm 20$    |          | V                   |
| $I_D$          | Maximum Drain Current - Continuous (Note 1)        | 100         |          | A                   |
|                |                                                    | 75          |          |                     |
|                | - Pulsed (Note 1)                                  | 300         |          |                     |
| $P_D$          | Total Power Dissipation @ $T_C = 25^\circ\text{C}$ | 125         |          | W                   |
|                | Derate above $25^\circ\text{C}$                    | 0.85        |          | W/ $^\circ\text{C}$ |
| $T_J, T_{STG}$ | Operating and Storage Junction Temperature Range   | -65 to +175 |          | $^\circ\text{C}$    |

### Thermal Characteristics

|                 |                                         |      |                    |
|-----------------|-----------------------------------------|------|--------------------|
| $R_{\theta JC}$ | Thermal Resistance, Junction-to-Case    | 1.2  | $^\circ\text{C/W}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient | 62.5 | $^\circ\text{C/W}$ |

### Package Outlines and Ordering Information

| Device Marking | Device   | Reel Size | Tape Width | Quantity |
|----------------|----------|-----------|------------|----------|
| FDB7045L       | FDB7045L | 13"       | 24mm       | 800      |
| FDP7045L       | FDP7045L | Tube      | N/A        | 45       |

**Electrical Characteristics** $T_C = 25^\circ\text{C}$  unless otherwise noted

| Symbol | Parameter | Test Conditions | Min | Typ | Max | Units |
|--------|-----------|-----------------|-----|-----|-----|-------|
|--------|-----------|-----------------|-----|-----|-----|-------|

**Off Characteristics**

|                                      |                                           |                                                                   |    |    |      |                      |
|--------------------------------------|-------------------------------------------|-------------------------------------------------------------------|----|----|------|----------------------|
| $BV_{DSS}$                           | Drain-Source Breakdown Voltage            | $V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$               | 30 |    |      | V                    |
| $\frac{\Delta BV_{DSS}}{\Delta T_J}$ | Breakdown Voltage Temperature Coefficient | $I_D = 250\text{ }\mu\text{A}$ , Referenced to $25^\circ\text{C}$ |    | 22 |      | mV/ $^\circ\text{C}$ |
| $I_{DSS}$                            | Zero Gate Voltage Drain Current           | $V_{DS} = 24\text{ V}, V_{GS} = 0\text{ V}$                       |    |    | 1    | $\mu\text{A}$        |
| $I_{GSSF}$                           | Gate-Body Leakage Current, Forward        | $V_{GS} = 20\text{ V}, V_{DS} = 0\text{ V}$                       |    |    | 100  | nA                   |
| $I_{GSSR}$                           | Gate-Body Leakage Current, Reverse        | $V_{GS} = -20\text{ V}, V_{DS} = 0\text{ V}$                      |    |    | -100 | nA                   |

**On Characteristics** (Note 2)

|                                        |                                                |                                                                                                                                                                 |    |                            |                            |                      |
|----------------------------------------|------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------|----|----------------------------|----------------------------|----------------------|
| $V_{GS(th)}$                           | Gate Threshold Voltage                         | $V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$                                                                                                                 | 1  | 1.5                        | 3                          | V                    |
| $\frac{\Delta V_{GS(th)}}{\Delta T_J}$ | Gate Threshold Voltage Temperature Coefficient | $I_D = -250\text{ }\mu\text{A}$ , Referenced to $25^\circ\text{C}$                                                                                              |    | -5                         |                            | mV/ $^\circ\text{C}$ |
| $R_{DS(on)}$                           | Static Drain-Source On-Resistance              | $V_{GS} = 10\text{ V}, I_D = 50\text{ A}$ ,<br>$V_{GS} = 10\text{ V}, I_D = 50\text{ A}, T_J = 125^\circ\text{C}$<br>$V_{GS} = 4.5\text{ V}, I_D = 40\text{ A}$ |    | 0.0039<br>0.0056<br>0.0048 | 0.0045<br>0.0070<br>0.0060 | $\Omega$             |
| $I_{D(on)}$                            | On-State Drain Current                         | $V_{GS} = 10\text{ V}, V_{DS} = 10\text{ V}$                                                                                                                    | 50 |                            |                            | A                    |
| $g_{FS}$                               | Forward Transconductance                       | $V_{DS} = 5\text{ V}, I_D = 50\text{ A}$                                                                                                                        |    | 120                        |                            | S                    |

**Dynamic Characteristics**

|           |                              |                                                                       |  |      |  |    |
|-----------|------------------------------|-----------------------------------------------------------------------|--|------|--|----|
| $C_{iss}$ | Input Capacitance            | $V_{DS} = 15\text{ V}, V_{GS} = 0\text{ V}$ ,<br>$f = 1.0\text{ MHz}$ |  | 5400 |  | pF |
| $C_{oss}$ | Output Capacitance           |                                                                       |  | 1170 |  | pF |
| $C_{rss}$ | Reverse Transfer Capacitance |                                                                       |  | 530  |  | pF |

**Switching Characteristics** (Note 2)

|              |                     |                                                                       |  |     |     |    |
|--------------|---------------------|-----------------------------------------------------------------------|--|-----|-----|----|
| $t_{d(on)}$  | Turn-On Delay Time  | $V_{DD} = 15\text{ V}, I_D = 50\text{ A}$ ,<br>$V_{GS} = 10\text{ V}$ |  | 14  | 30  | ns |
| $t_r$        | Turn-On Rise Time   |                                                                       |  | 114 | 160 | ns |
| $t_{d(off)}$ | Turn-Off Delay Time |                                                                       |  | 105 | 150 | ns |
| $t_f$        | Turn-Off Fall Time  |                                                                       |  | 115 | 160 | ns |
| $Q_g$        | Total Gate Charge   | $V_{DS} = 15\text{ V}$ ,<br>$I_D = 50\text{ A}, V_{GS} = 5\text{ V}$  |  | 50  | 70  | nC |
| $Q_{gs}$     | Gate-Source Charge  |                                                                       |  | 16  |     | nC |
| $Q_{gd}$     | Gate-Drain Charge   |                                                                       |  | 16  |     | nC |

**Drain-Source Diode Characteristics and Maximum Ratings**

|          |                                                       |                                            |  |      |     |   |
|----------|-------------------------------------------------------|--------------------------------------------|--|------|-----|---|
| $I_S$    | Maximum Continuous Drain-Source Diode Forward Current | (Note 2)                                   |  |      | 75  | A |
| $V_{SD}$ | Drain-Source Diode Forward Voltage                    | $V = 0\text{ V}, I = 50\text{ A}$ (Note 2) |  | 0.95 | 1.2 | V |

**Notes:**

1. Calculated continuous current based on maximum allowable junction temperature. Actual maximum continuous current limited by package constraints to 75A.
2. Pulse Test: Pulse Width  $\leq 300\text{ }\mu\text{s}$ , Duty Cycle  $\leq 2.0\%$

## Typical Characteristics

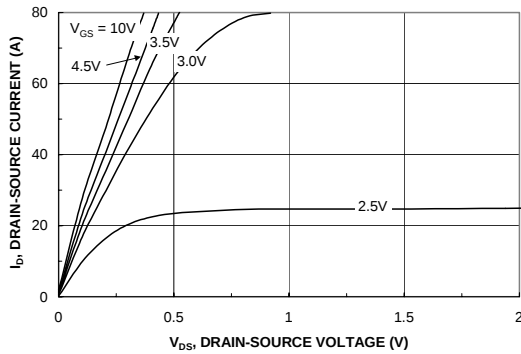


Figure 1. On-Region Characteristics.

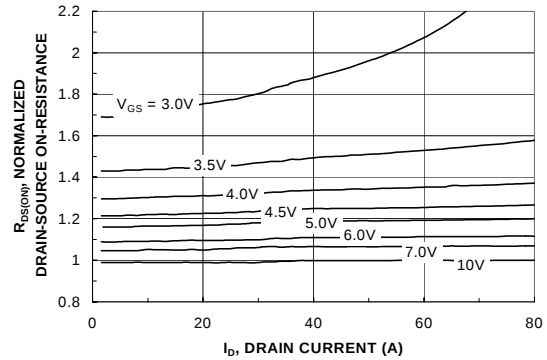


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

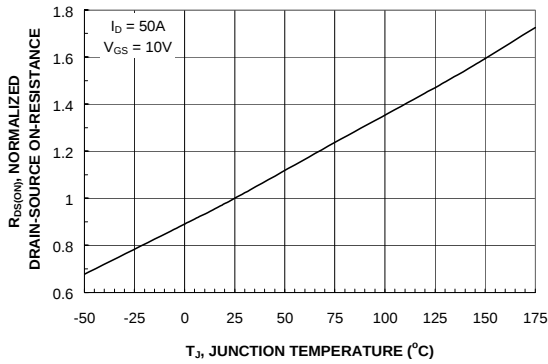


Figure 3. On-Resistance Variation with Temperature.

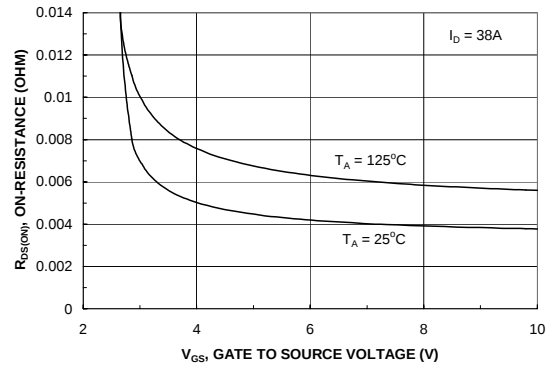


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

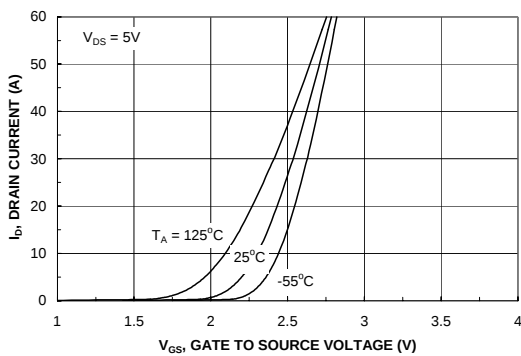


Figure 5. Transfer Characteristics.

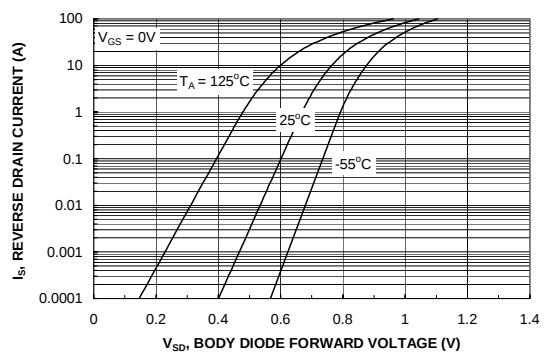


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

## Typical Characteristics (continued)

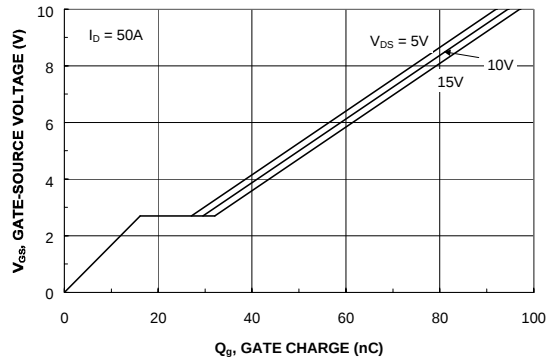


Figure 7. Gate-Charge Characteristics.

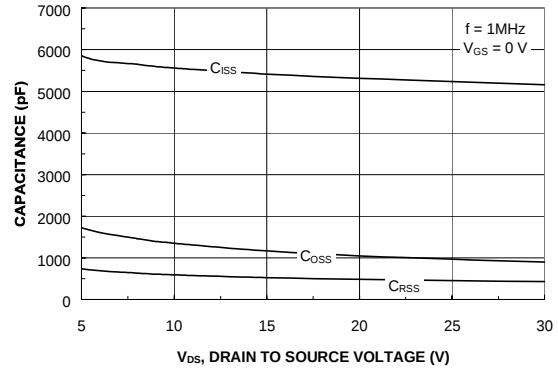


Figure 8. Capacitance Characteristics.

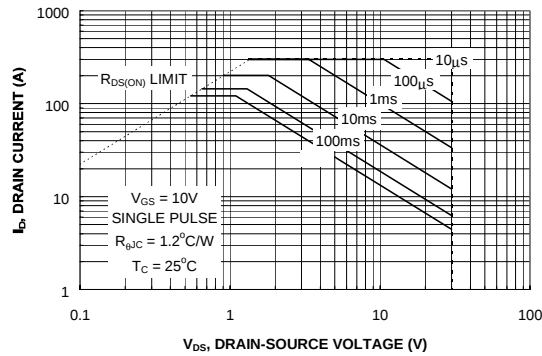


Figure 9. Maximum Safe Operating Area.

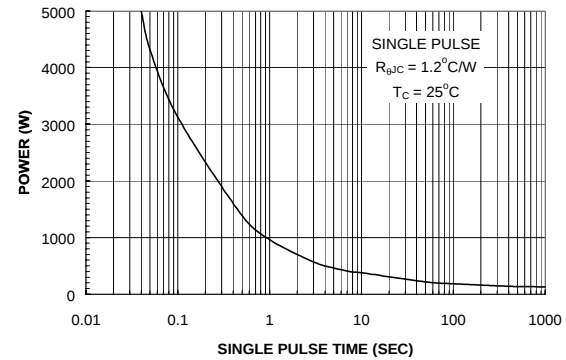


Figure 10. Single Pulse Maximum Power Dissipation.

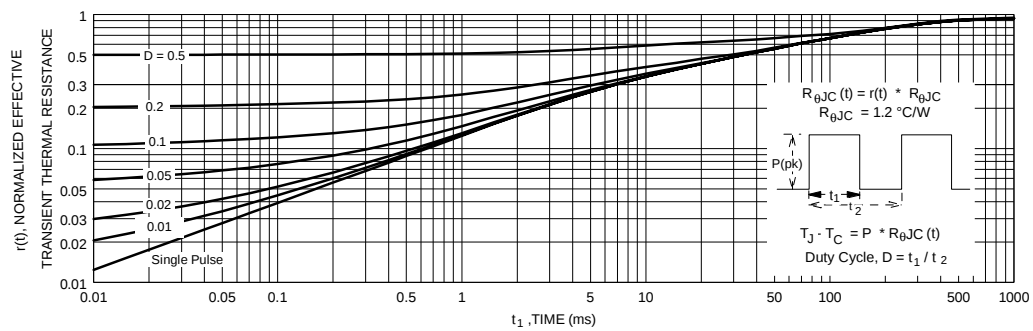


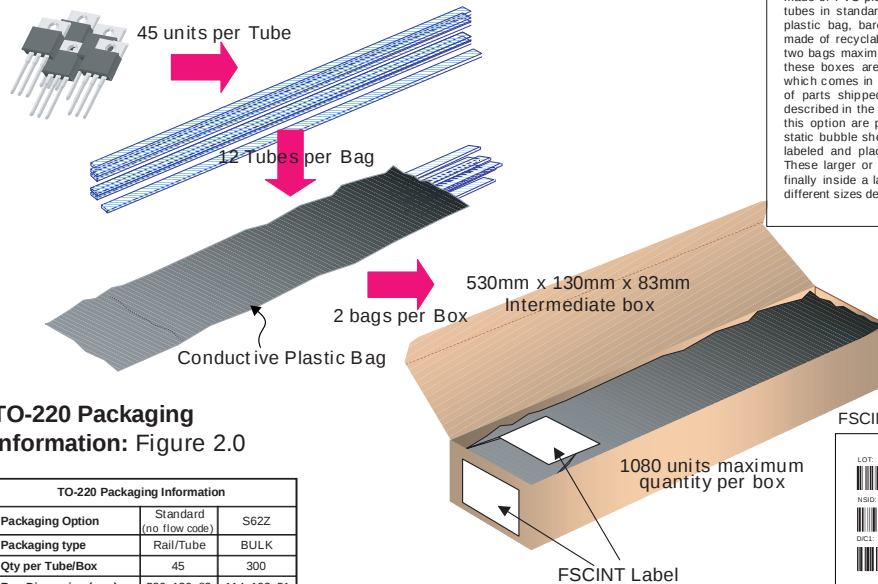
Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1.  
Transient thermal response will change depending on the circuit board design.

## TO-220 Tape and Reel Data and Package Dimensions

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### TO-220 Tube Packing Configuration: Figure 1.0



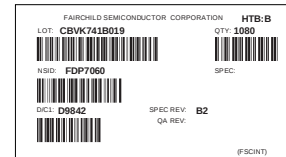
#### Packaging Description:

TO-220 parts are shipped normally in tube. The tube is made of PVC plastic treated with anti-static agent. These tubes in standard option are placed inside a dissipative plastic bag, barcode labeled, and placed inside a box made of recyclable corrugated paper. One box contains two bags maximum (see fig. 1.0). And one or several of these boxes are placed inside a labeled shipping box which comes in different sizes depending on the number of parts shipped. The other option comes in bulk as described in the Packaging Information table. The units in this option are placed inside a small box laid with anti-static bubble sheet. These smaller boxes are individually labeled and placed inside a larger box (see fig. 3.0). These larger or intermediate boxes then will be placed finally inside a labeled shipping box which still comes in different sizes depending on the number of units shipped.

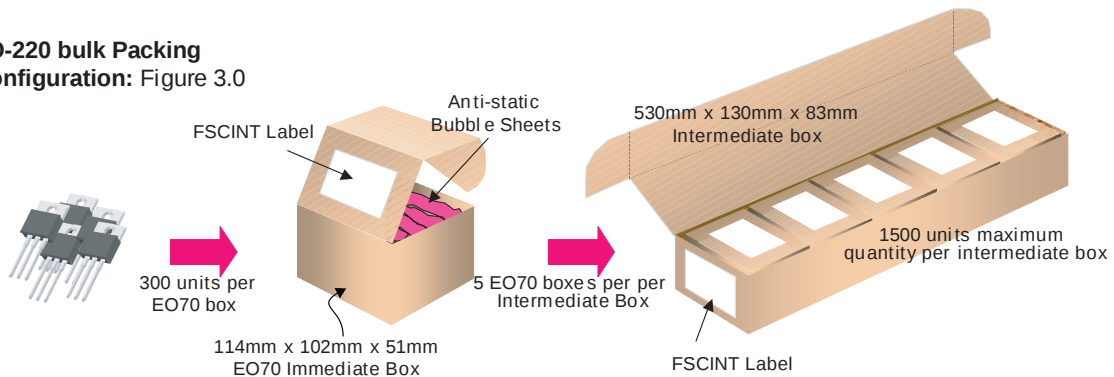
### TO-220 Packaging Information: Figure 2.0

| TO-220 Packaging Information |                         |            |
|------------------------------|-------------------------|------------|
| Packaging Option             | Standard (no flow code) | S62Z       |
| Packaging type               | Rail/Tube               | BULK       |
| Qty per Tube/Box             | 45                      | 300        |
| Box Dimension (mm)           | 530x130x83              | 114x102x51 |
| Max qty per Box              | 1,080                   | 1,500      |
| Weight per unit (gm)         | 1.4378                  | 1.4378     |
| Note/Comments                |                         |            |

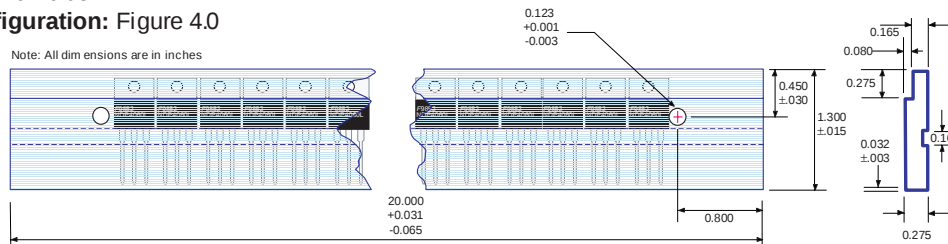
#### FSCINT Label sample



### TO-220 bulk Packing Configuration: Figure 3.0

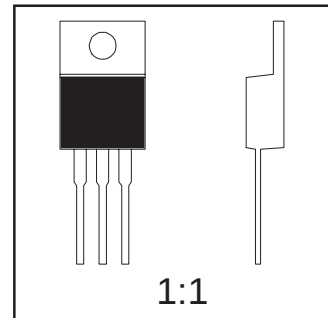
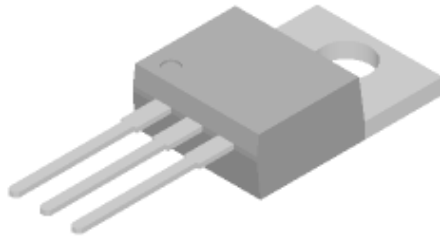


### TO-220 Tube Configuration: Figure 4.0



## TO-220 Tape and Reel Data and Package Dimensions, continued

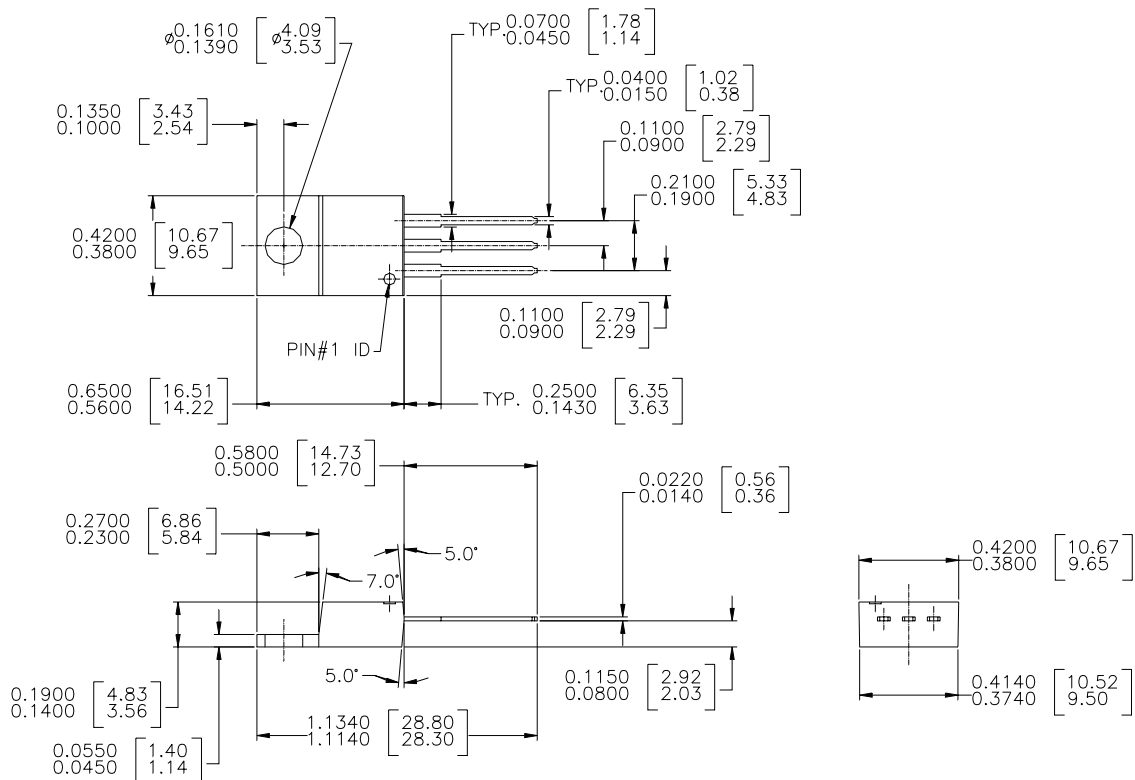
### TO-220 (FS PKG Code 37)



Scale 1:1 on letter size paper

Dimensions shown below are in:  
inches [millimeters]

Part Weight per unit (gram): 1.4378



NOTE : UNLESS OTHERWISE SPECIFIED

1. STANDARD LEAD FINISH :  
200 MICROINCHES / 5.08 MICRON MINIMUM  
LEAD / TIN 15/85 ON OLIN 194 COPPER OR EQUIVALENT

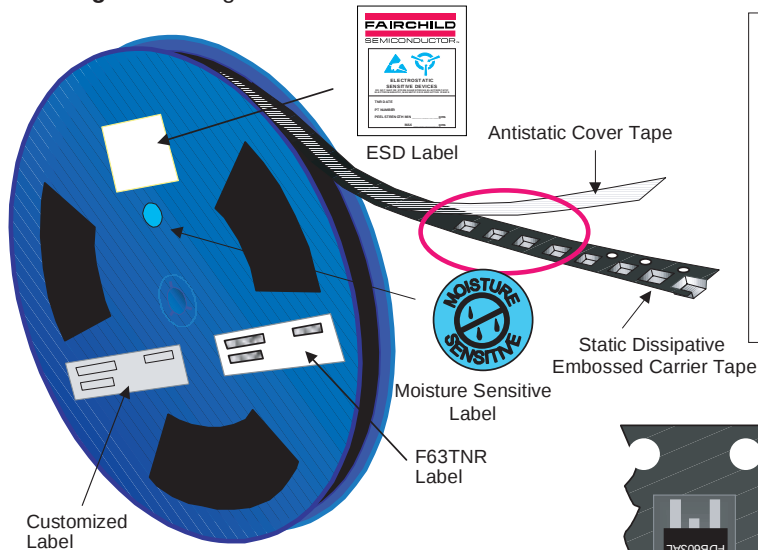
2. DIMENSION BASED ON JEDEC STANDARD TO-220  
VARIATION AB, ISSUE J, DATED 3/24/87

TO 220 3 LEAD

# TO-263AB/D<sup>2</sup>PAK Tape and Reel Data and Package Dimensions



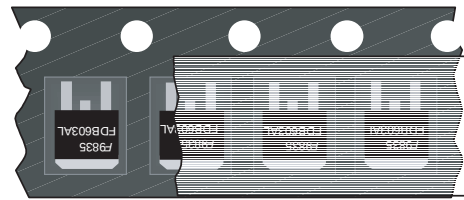
## TO-263AB/D<sup>2</sup>PAK Packaging Configuration: Figure 1.0



### Packaging Description:

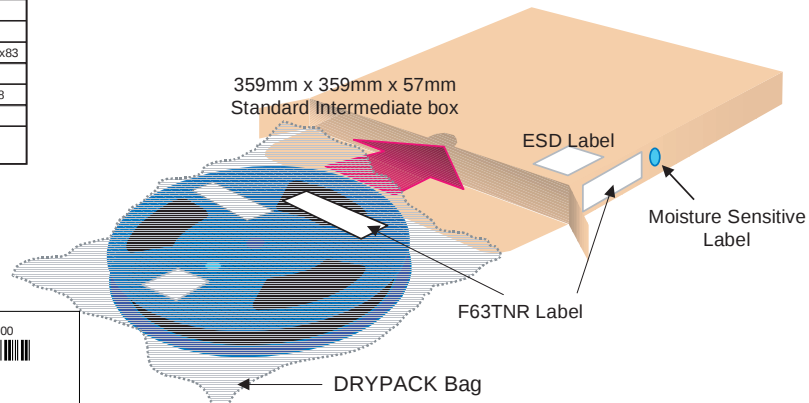
TO-263/D<sup>2</sup>PAK parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 800 units per 13" or 330cm diameter reel. The reels are dark blue in color and is made of polystyrene plastic (anti-static coated). This and some other options are further described in the Packaging Information table.

These full reels are individually barcode labeled, dry packed, and placed inside a standard intermediate box (illustrated in figure 1.0) made of recyclable corrugated brown paper. One box contains one reel maximum. And these boxes are placed inside a barcode labeled shipping box which comes in different sizes depending on the number of parts shipped.



### TO-263AB/D<sup>2</sup>PAK Unit Orientation

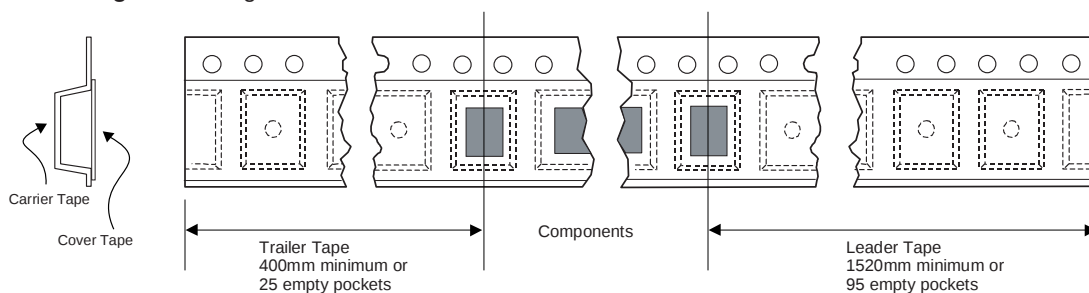
| TO-263AB/D <sup>2</sup> PAK Packaging Information |                         |            |
|---------------------------------------------------|-------------------------|------------|
| Packaging Option                                  | Standard (no flow code) | L86Z       |
| Packaging type                                    | TNR                     | Rail/Tube  |
| Qty per Reel/Tube/Bag                             | 800                     | 45         |
| Reel Size                                         | 13" Dia                 | -          |
| Box Dimension (mm)                                | 359x359x57              | 530x130x83 |
| Max qty per Box                                   | 800                     | 1,080      |
| Weight per unit (gm)                              | 1.4378                  | 1.4378     |
| Weight per Reel                                   | 1.6050                  | -          |
| Note/Comments                                     |                         |            |



### F63TNR Label sample

|                  |           |
|------------------|-----------|
| LOT: CBVK741B019 | QTY: 800  |
| FSID: FDB6320L   | SPEC:     |
| D/C1: D9842      | QTY1:     |
| D/C2:            | QTY2:     |
| SPEC REV: CPN:   | N/F: F    |
|                  | (F63TNR)3 |

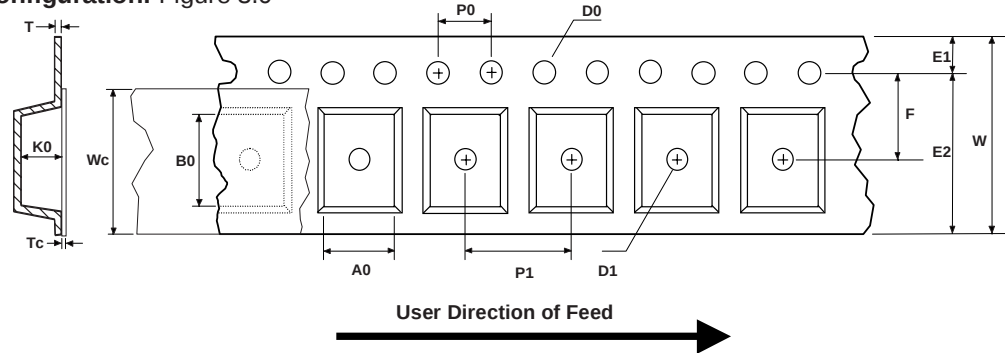
## TO-263AB/D<sup>2</sup>PAK Tape Leader and Trailer Configuration: Figure 2.0



## TO-263AB/D<sup>2</sup>PAK Tape and Reel Data and Package Dimensions, continued

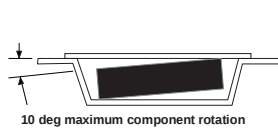
### TO-263AB/D<sup>2</sup>PAK Embossed Carrier Tape

Configuration: Figure 3.0

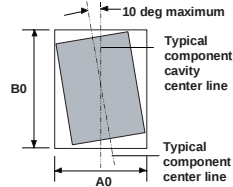


| Dimensions are in millimeter             |                  |                  |                |                 |                 |                 |              |                  |                |               |                 |                   |                |                 |
|------------------------------------------|------------------|------------------|----------------|-----------------|-----------------|-----------------|--------------|------------------|----------------|---------------|-----------------|-------------------|----------------|-----------------|
| Pkg type                                 | A0               | B0               | W              | D0              | D1              | E1              | E2           | F                | P1             | P0            | K0              | T                 | Wc             | Tc              |
| TO263AB/<br>D <sup>2</sup> PAK<br>(24mm) | 10.60<br>+/-0.10 | 15.80<br>+/-0.10 | 24.0<br>+/-0.3 | 1.55<br>+/-0.05 | 1.60<br>+/-0.10 | 1.75<br>+/-0.10 | 22.25<br>min | 11.50<br>+/-0.10 | 16.0<br>+/-0.1 | 4.0<br>+/-0.1 | 4.90<br>+/-0.10 | 0.450<br>+/-0.150 | 21.0<br>+/-0.3 | 0.06<br>+/-0.02 |

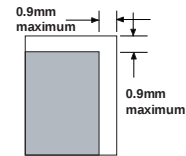
Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).



Sketch A (Side or Front Sectional View)  
Component Rotation

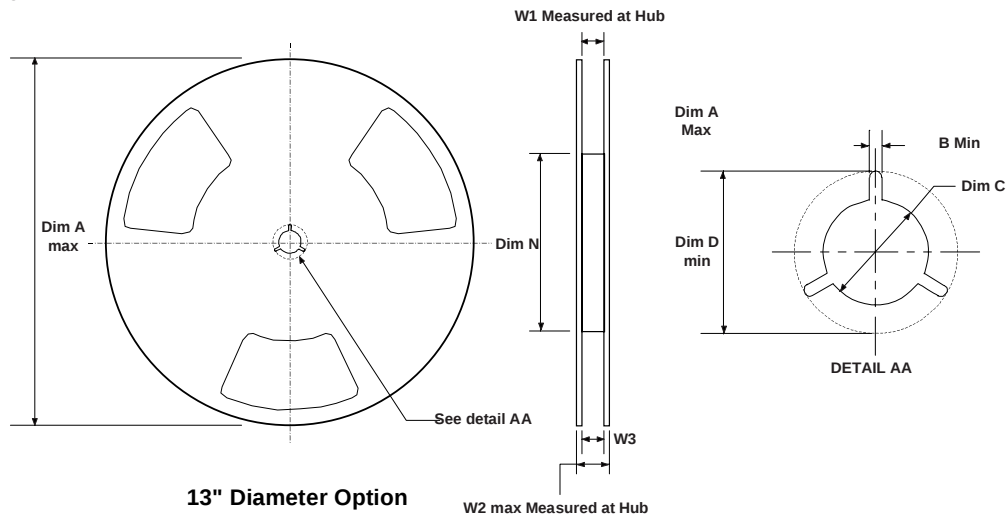


Sketch B (Top View)  
Component Rotation



Sketch C (Top View)  
Component lateral movement

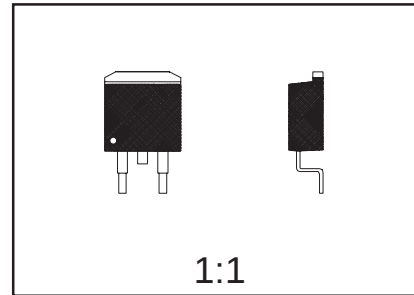
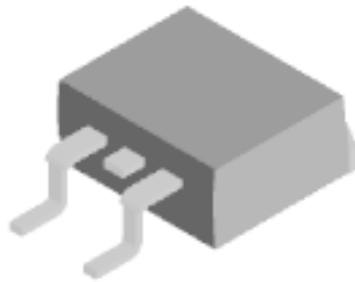
### TO-263AB/D<sup>2</sup>PAK Reel Configuration: Figure 4.0



| Dimensions are in inches and millimeters |             |              |              |                                   |               |             |                                  |               |                                |
|------------------------------------------|-------------|--------------|--------------|-----------------------------------|---------------|-------------|----------------------------------|---------------|--------------------------------|
| Tape Size                                | Reel Option | Dim A        | Dim B        | Dim C                             | Dim D         | Dim N       | Dim W1                           | Dim W2        | Dim W3 (LSL-USL)               |
| 24mm                                     | 13" Dia     | 13.00<br>330 | 0.059<br>1.5 | 512 +0.020/-0.008<br>13 +0.5/-0.2 | 0.795<br>20.2 | 4.00<br>100 | 0.961 +0.078/-0.000<br>24.4 +2/0 | 1.197<br>30.4 | 0.941 - 0.1.079<br>23.9 - 27.4 |

# TO-263AB/D<sup>2</sup>PAK Tape and Reel Data and Package Dimensions, continued

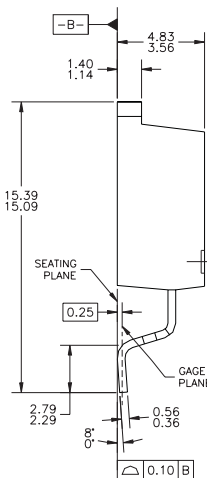
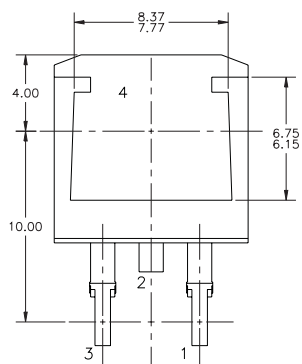
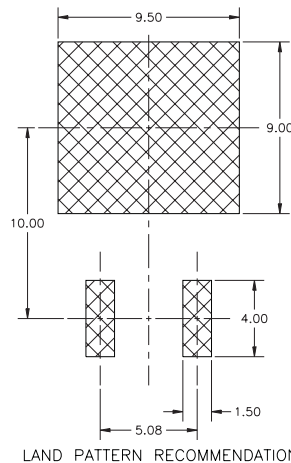
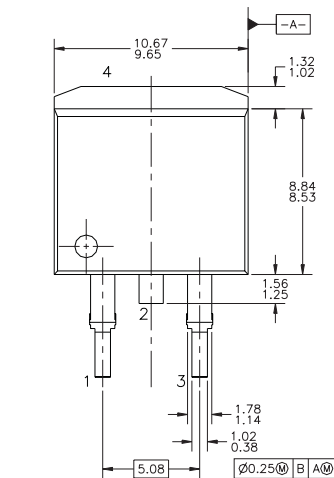
## TO-263AB/D<sup>2</sup>PAK (FS PKG Code 45)



Scale 1:1 on letter size paper

Dimensions shown below are in:  
inches [millimeters]

Part Weight per unit (gram): 1.4378



- NOTES: UNLESS OTHERWISE SPECIFIED  
A) ALL DIMENSIONS ARE IN MILLIMETERS.  
B) STANDARD LEAD FINISH:  
200 MICROINCHES / 5.08 MICROMETERS MIN.  
LEAD/TIN 15/85 ON OLIN 194 COPPER OR  
EQUIVALENT.  
C) MAXIMUM VERTICAL BURR ON HEATSINK NOT  
TO EXCEED 0.003 INCH / 0.05mm.  
D) NO PACKAGE CHIPS, CRACKS OR SURFACE  
IDENTIFICATION ALLOWED AFTER FORMING.  
E) REFERENCE JEDEC, TO-263, ISSUE C,  
VARIATION AB, DATED 2/92.

## TRADEMARKS

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FACT Quiet Series™  
FAST®  
FASTr™  
GTO™  
HiSeC™

ISOPLANAR™  
MICROWIRE™  
POP™  
PowerTrench®  
QFET™  
QS™  
Quiet Series™  
SuperSOT™-3  
SuperSOT™-6  
SuperSOT™-8

SyncFET™  
TinyLogic™  
UHC™  
VCX™

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|--------------------------|------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
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