

N-Channel Enhancement Mode MOSFET

TDM3458

**DESCRIPTION**

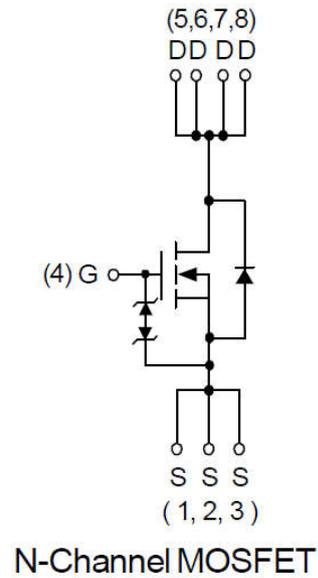
The TDM3458 uses advanced trench technology to provide excellent RDS(ON) and low gate charge. This device is suitable for use as a load switch or in PWM applications.

**GENERAL FEATURES**

- RDS(ON) < 8.7mΩ @ VGS=4.5V  
RDS(ON) < 5.5mΩ @ VGS=10V
- High Power and current handling capability
- Lead free product is available
- ESD protection
- Surface Mount Package

**Application**

- PWM applications
- Load switch
- Power management



**ABSOLUTE MAXIMUM RATINGS**( $T_A=25^{\circ}C$  unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current @ Continuous	I <sub>D</sub> (T <sub>C</sub> =25°C)	60	A
	I <sub>D</sub> (T <sub>C</sub> =100°C)	38	A
Drain Current @ Current-Pulsed (Note 1)	I <sub>DM</sub> (T <sub>C</sub> =25°C)	120	A
Maximum Power Dissipation	P <sub>D</sub> (T <sub>C</sub> =25°C)	29.8	W
	P <sub>D</sub> (T <sub>C</sub> =100°C)	11.9	W
Drain Current @ Continuous (Note 2)	I <sub>D</sub> (T <sub>A</sub> =25°C)	17.4	A
	I <sub>D</sub> (T <sub>A</sub> =70°C)	13.9	A
Maximum Power Dissipation (Note 2)	P <sub>D</sub> (T <sub>A</sub> =25°C)	2.5	W
	P <sub>D</sub> (T <sub>A</sub> =70°C)	1.6	W
Maximum Operating Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 To 150	°C

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THERMAL CHARACTERISTICS

Thermal Resistance,Junction-to-Ambient (Note 2)	R $\theta$ JA(t $\leq$ 10s)	20	°C/W
	R $\theta$ JA(Steady State)	50	°C/W
Thermal Resistance,Junction-to-Case	R $\theta$ JC(Steady State)	4.2	°C/W

ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C unless otherwise noted)

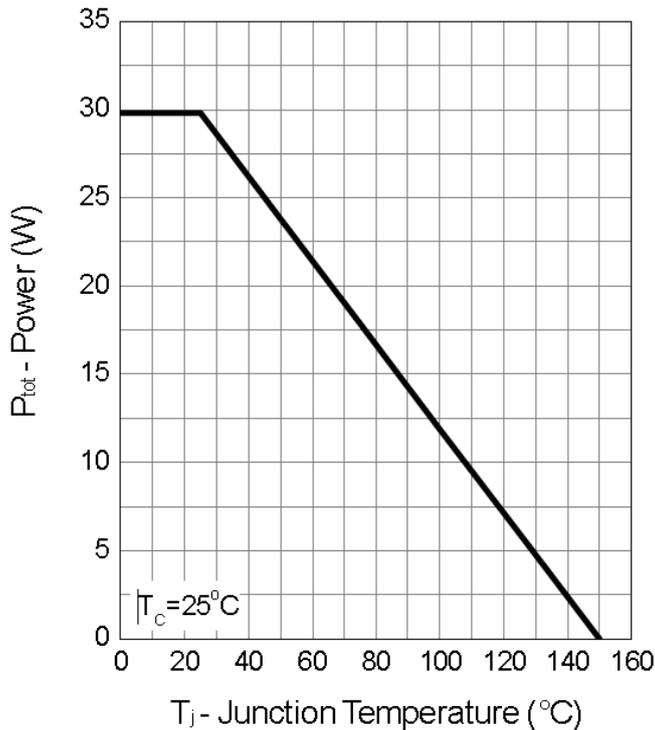
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	30	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =24V,V <sub>GS</sub> =0V	-	-	1	μ A
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	± 10	μ A
<b>ON CHARACTERISTICS (Note 3)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	1.4	1.7	2.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	6.7	8.7	mΩ
		V <sub>GS</sub> =10V, I <sub>D</sub> =15A	-	4.5	5.5	mΩ
		T <sub>J</sub> =125°C	-	6.75	-	mΩ
<b>DYNAMIC CHARACTERISTICS (Note4)</b>						
Gate Resistance	R <sub>G</sub>	V <sub>DS</sub> =0V,V <sub>GS</sub> =0V, F=1.0MHz	-	1.6	2.5	Ω
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V, F=1.0MHz	-	780	1010	PF
Output Capacitance	C <sub>oss</sub>		-	510	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	39	-	PF
<b>SWITCHING CHARACTERISTICS (Note 4)</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DS</sub> =15V, R <sub>L</sub> =15 Ω, V <sub>GEN</sub> =10V, R <sub>G</sub> =1 Ω I <sub>D</sub> =1A	-	11	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	8	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	19.6	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	17	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =15V,I <sub>D</sub> =15A,V <sub>GS</sub> =4.5V	-	5.8	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	3	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	1.5	-	nC
Body Diode Reverse Recovery Time	T <sub>rr</sub>	I <sub>F</sub> =5A, di/dt=100A/μs	-	25	-	nS
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	11.8	-	nC
<b>DRAIN-SOURCE DIODE CHARACTERISTICS</b>						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =20A	-	0.81	1.1	V

NOTES:

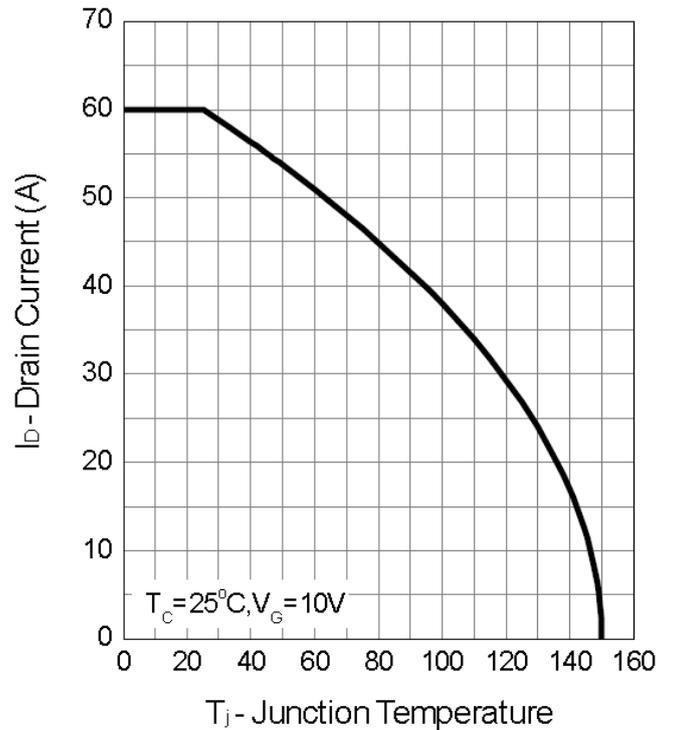
1. Pulse width limited by max. junction temperature.
2. R $\theta$ JA steady state t=100s.
3. Pulse Test: Pulse Width  $\leq$  300μs, Duty Cycle  $\leq$  2%.
4. Guaranteed by design, not subject to production testing

Typical Operating Characteristics

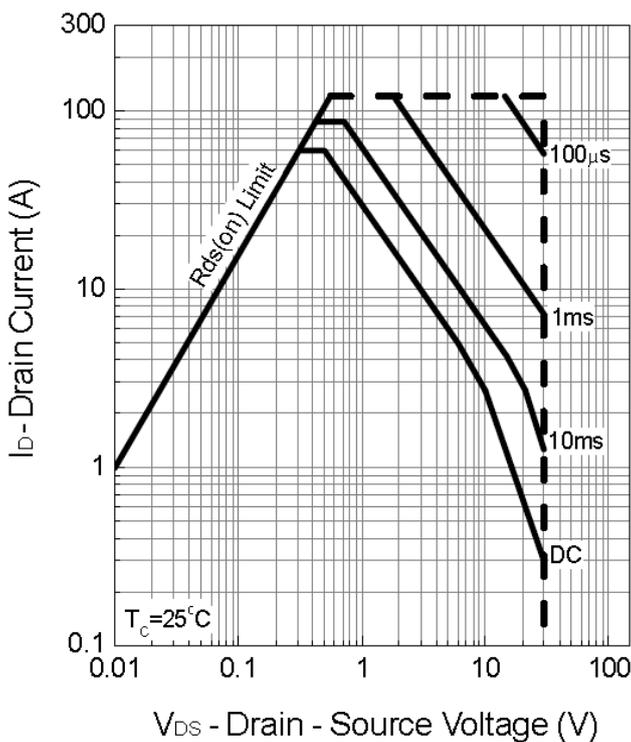
Power Dissipation



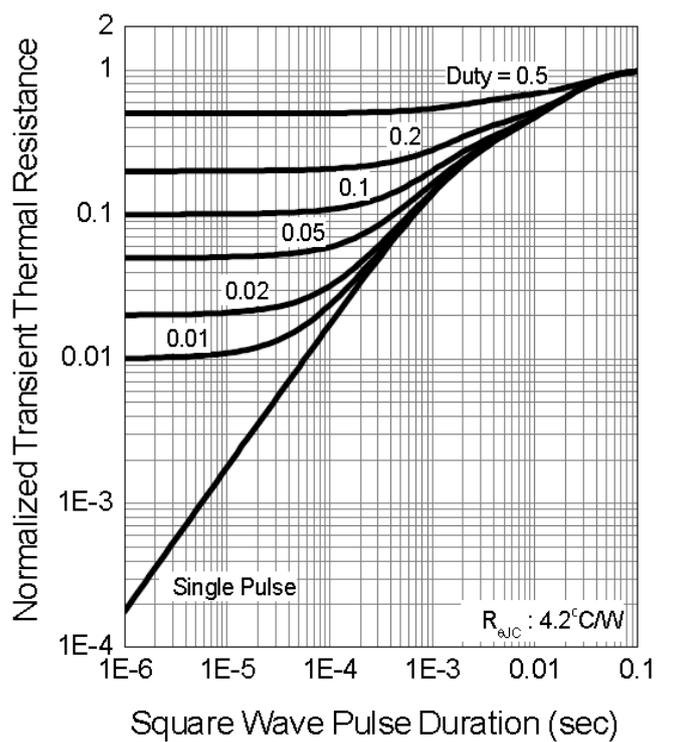
Drain Current



Safe Operation Area

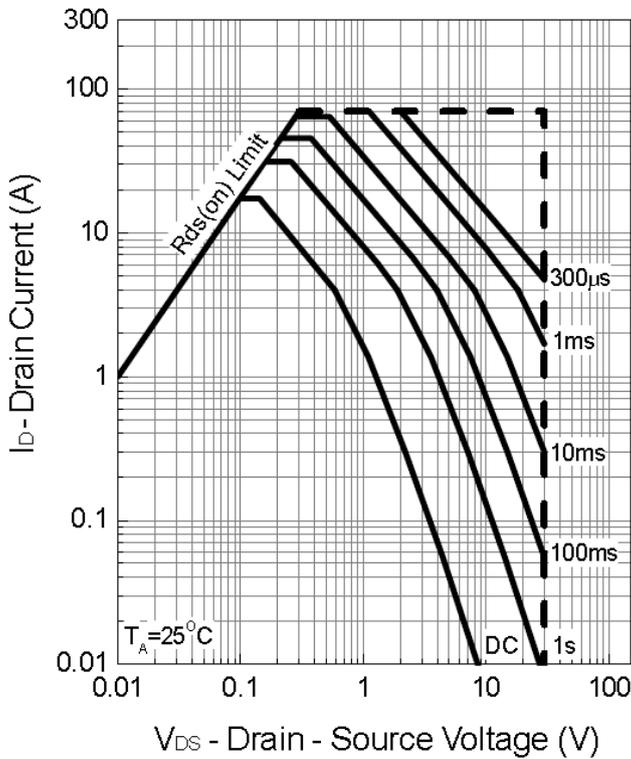


Thermal Transient Impedance

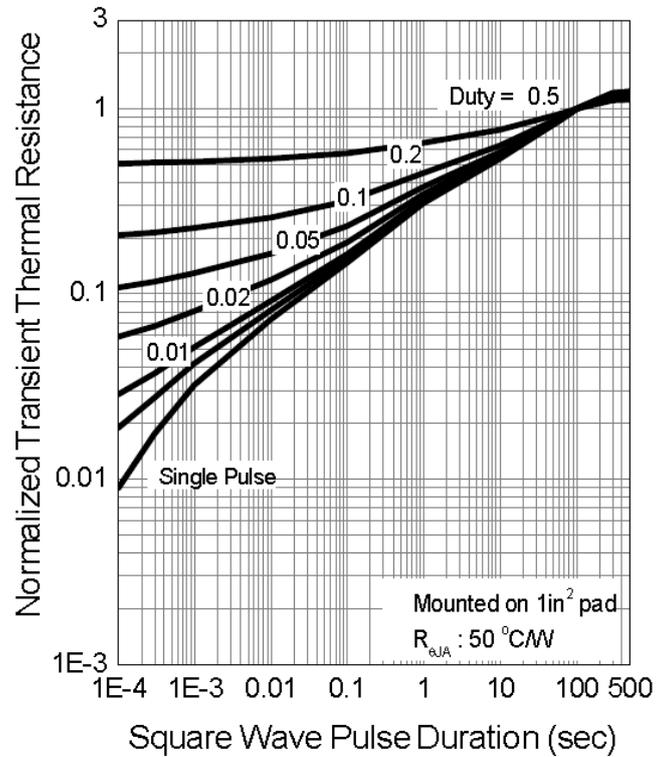


Typical Operating Characteristics(Cont.)

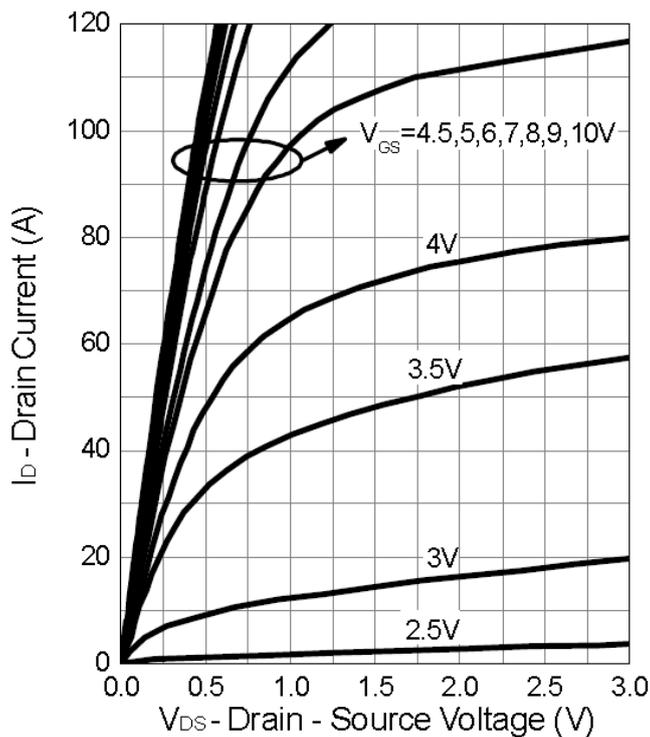
Safe Operation Area



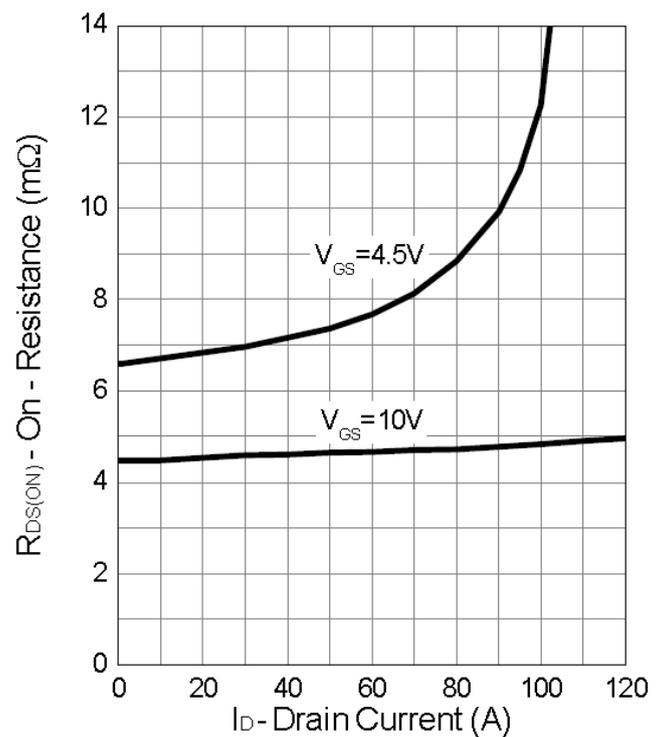
Thermal Transient Impedance



Output Characteristics

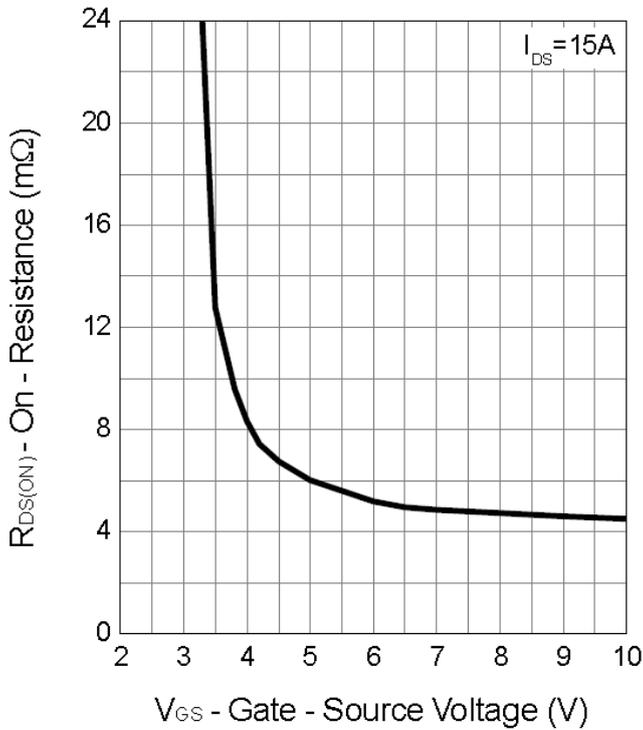


Drain-Source On Resistance

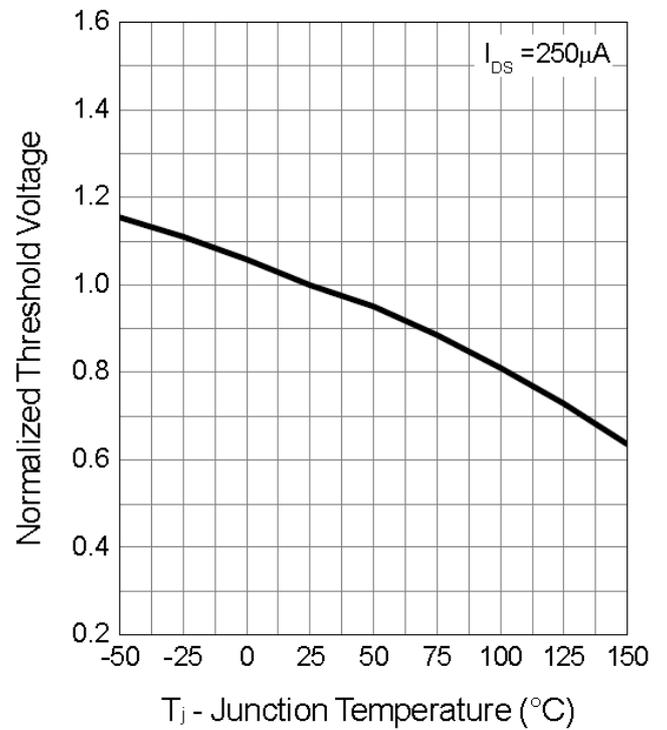


Typical Operating Characteristics (Cont.)

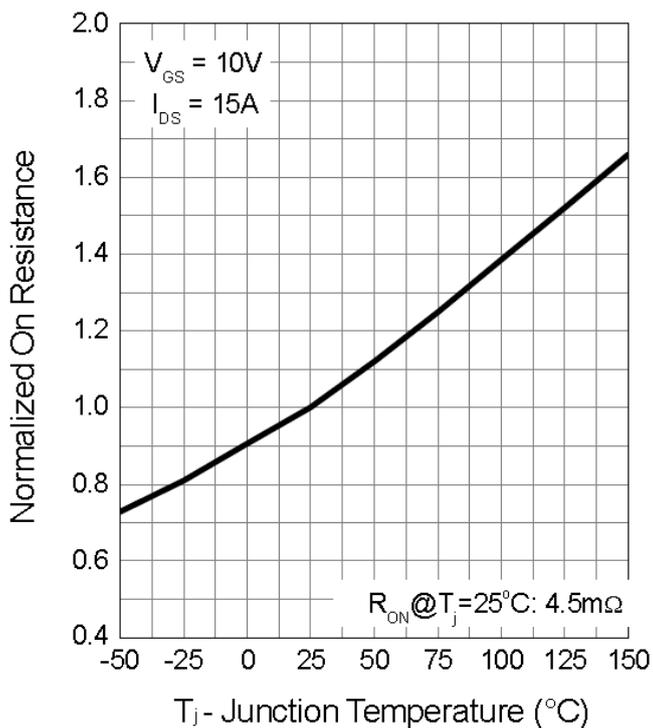
Gate-Source On Resistance



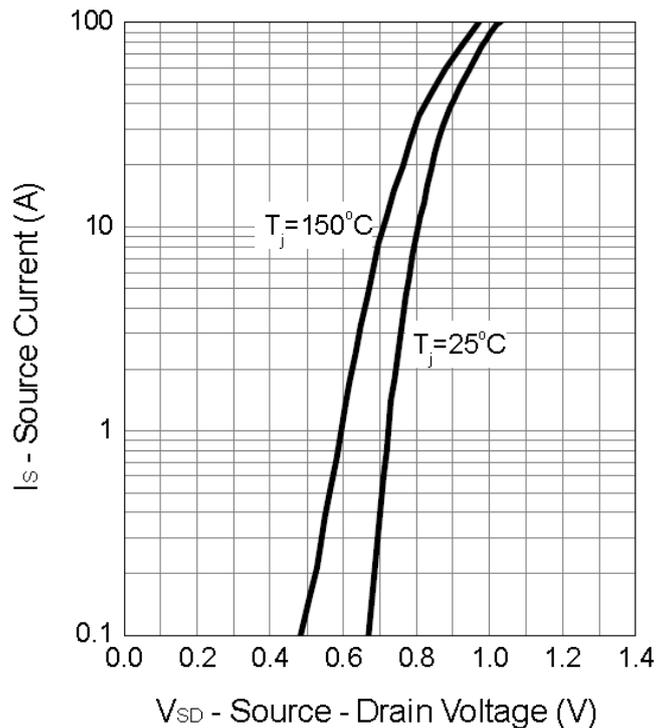
Gate Threshold Voltage



Drain-Source On Resistance

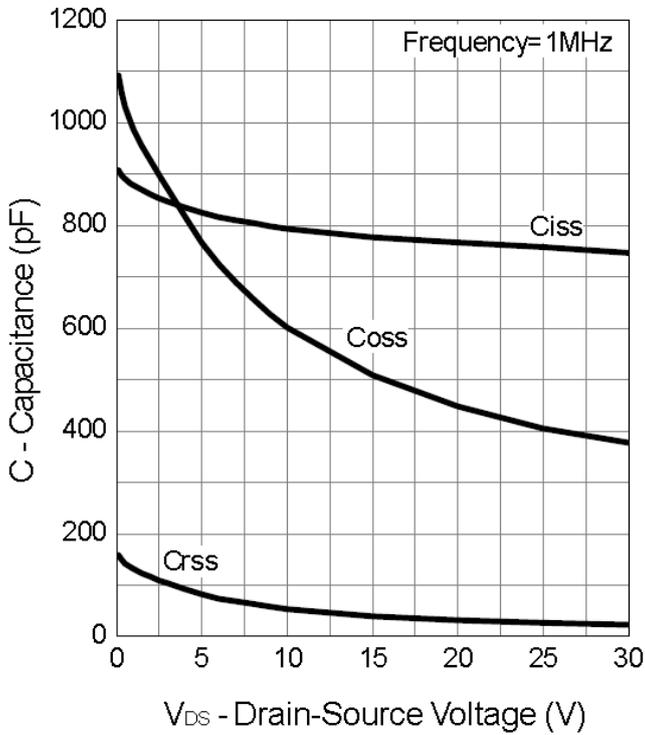


Source-Drain Diode Forward

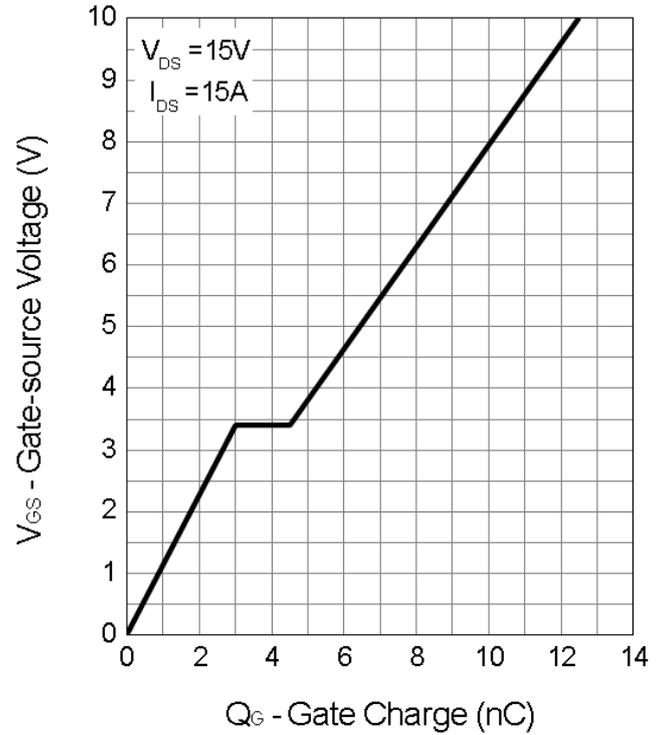


Typical Operating Characteristics (Cont.)

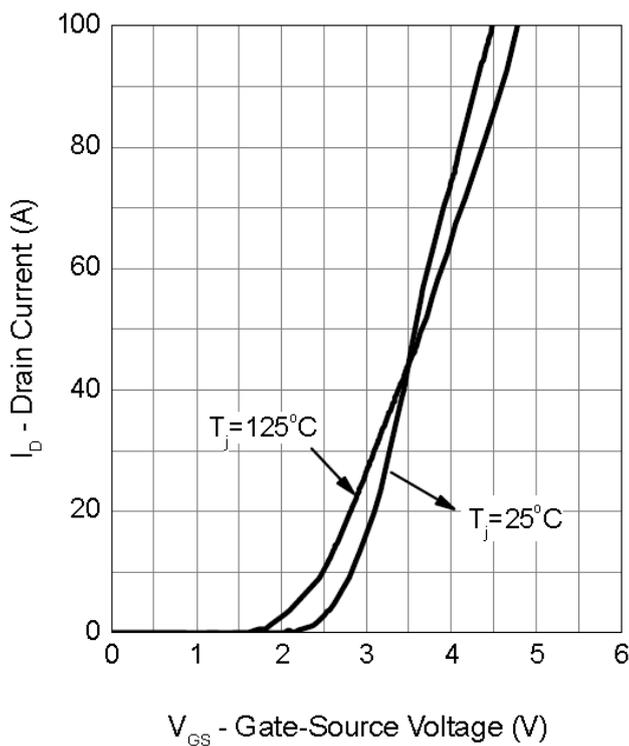
Capacitance



Gate Charge

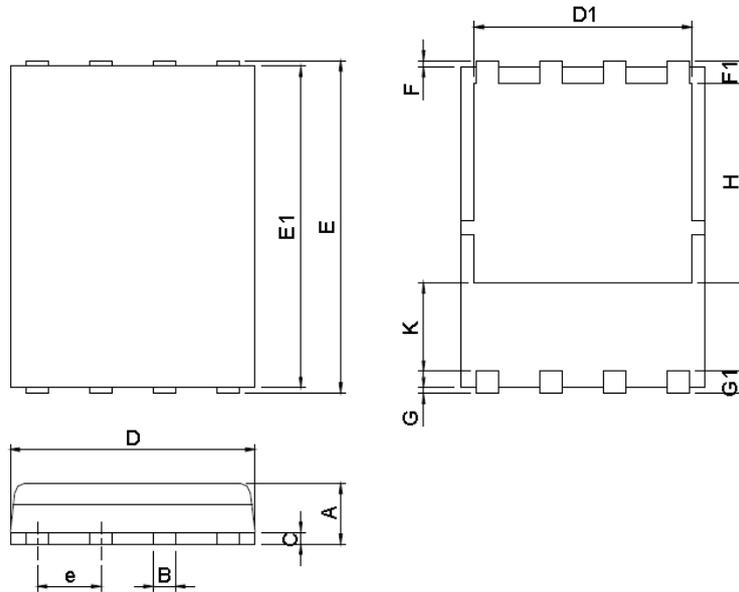


Transfer Characteristics



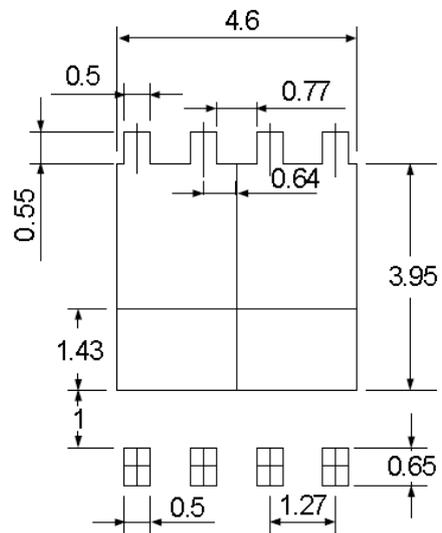
Package Information

DFN5\*6-8 Package



DIMENSIONS	DFN5x6-8			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.90	1.20	0.035	0.047
B	0.3	0.51	0.012	0.020
C	0.19	0.25	0.007	0.010
D	4.80	5.30	0.189	0.209
D1	4.00	4.40	0.157	0.173
E	5.90	6.20	0.232	0.244
E1	5.50	5.80	0.217	0.228
e	1.27 BSC		0.050 BSC	
F	0.05	0.30	0.002	0.012
F1	0.35	0.75	0.014	0.030
G	0.05	0.30	0.002	0.012
G1	0.35	0.75	0.014	0.030
H	3.34	3.9	0.131	0.154
K	0.762	-	0.03	-

RECOMMENDED LAND PATTERN



UNIT: mm

Note : 1.Dimension D, D1,D2 and E1 do not include mold flash or protrusions.  
Mold flash or protrusions shall not exceed 10 mil.

Design Notes