To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

MOS FIELD EFFECT TRANSISTORS

Phase-out/Discontinued

2SK2941

SWITCHING
N-CHANNEL POWER MOS FET
INDUSTRIAL USE

DESCRIPTION

This product is n-Chanel MOS Field Effect Transistor designed high current switching application.

FEATURE

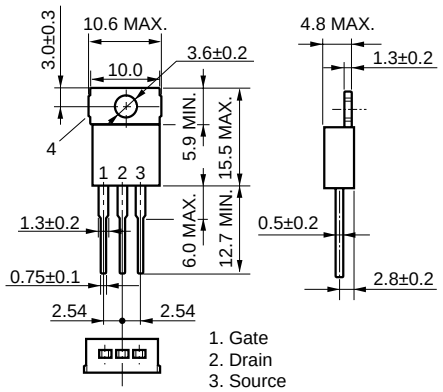
- Low On-Resistance
  $R_{DS(on)1}$ = 14 mΩ Typ. (V_{GS} = 10 V, I_D =18 A)
  $R_{DS(on)2}$ = 22 mΩ Typ. (V_{GS} = 4 V, I_D = 18 A)
- Low C_{iss} C_{iss} = 1250 pF Typ.
- Built-in G-S Protection Diode

ABSOLUTE MAXIMUM RATINGS (T_A = 25 ˚C)

Maximum Voltages and Currents			
Drain to Source Voltage	V_{DSS}	30	V
Gate to Source Voltage	V_{GSS}	±20	V
Drain Current (DC)	$I_{D(DC)}$	±35	A
Drain Current (Pulse)*	$I_{D(Pulse)}$	±140	A
Maximum Power Dissipation			
Total Power Dissipation (T_A = 25 ˚C)	P_T	1.5	W
Total Power Dissipation (T_C = 25 ˚C)	P_T	60	W
Maximum Temperature			
Channel Temperature	T_{ch}	150	˚C
Storage Temperature	T_{stg}	–55 to + 125	˚C

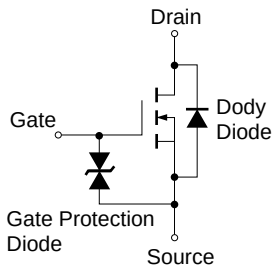
* PW ≤ 10 μs, Duty Cycle ≤ 1%

PACKAGE DIMENSIONS
inmillimeters



1. Gate
2. Drain
3. Source
4. Fin (Drain)
JEDEC: TO-220AB

MP-25 (TO-220)



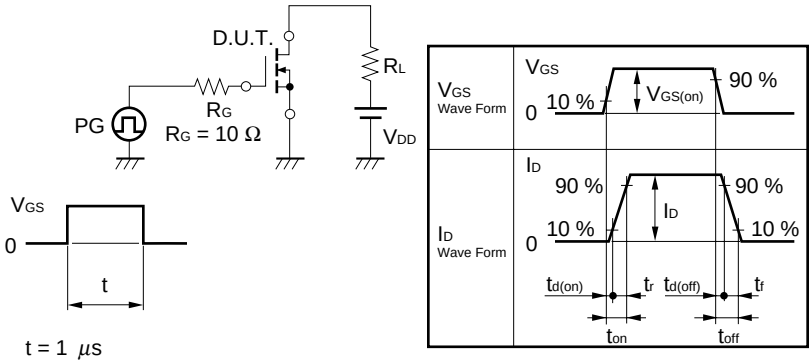
The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device acutally used, an addtional protection circuit is externally required if voltage exeeding the rated voltage may be applied to this device.

Document No. D11007EJ1V0DS00 (1st edition)
Date Published May 1997 N

ELECTRICAL CHARACTERISTICS (T_A = 25 °C)

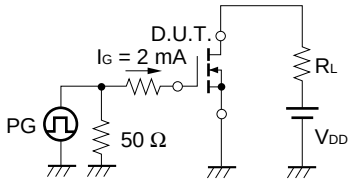
CHARACTERISTIC	SYMBLO	MIN.	TYP.	MAX.	UNIT	TEST CONDITION
Drain to Source On-State Resistance	$R_{DS(on)1}$		14	20	mΩ	V_{GS} = 10 V, I_D = 18 A
	$R_{DS(on)2}$		22	33	mΩ	V_{GS} = 4 V, I_D = 18 A
Gate to Source Cutoff Voltage	$V_{GS(off)}$	1.0	1.5	2.0	V	V_{DS} = 10 V, I_D = 1 mA
Forward Transfer Admittance	$\|y_{fs}\|$	8.0	25		S	V_{DS} = 10 V, I_D = 18 A
Drain Leakage Current	I_{DDS}			10	μA	V_{DS} = 30 V, V_{GS} = 0
Gate to Source Leakage Current	I_{GSS}			±10	μA	V_{GS} = ±20 V, V_{DS} = 0
Input Capacitance	C_{iss}		1250		pF	V_{DS} = 10 V, V_{GS} = 0, f =1 MHz
Output Capacitance	C_{oss}		900		pF	
Reverse Transfer Capacitance	C_{rss}		460		pF	
Turn-on Delay Time	$t_{d(on)}$		40		ns	I_D = 18 A, $V_{GS(on)}$ = 10 V V_{DD} = 15 V, R_G = 10 Ω
Rise Time	t_r		430		ns	
Turn-off Delay Time	$t_{d(off)}$		160		ns	
Fall Time	t_f		220		ns	
Total Gate Charge	Q_G		50		nC	I_D = 35 A, V_{DD} = 24 V, V_{GS} = 10 V
Gate to Source Charge	Q_{GS}		4.5		nC	
Gate to Drain Charge	Q_{GD}		21		nC	
Body Diode Forward Voltage	$V_{F(S-D)}$		1.0		V	I_F = 35 A, V_{GS} = 0
Reverse Recovery Time	t_{rr}		65		ns	I_F = 35 A, V_{GS} = 0, di/dt = 100 A/μs
Reverse Recovery Charge	Q_{rr}		90		nC	

Test Circuit 1 Switching Time



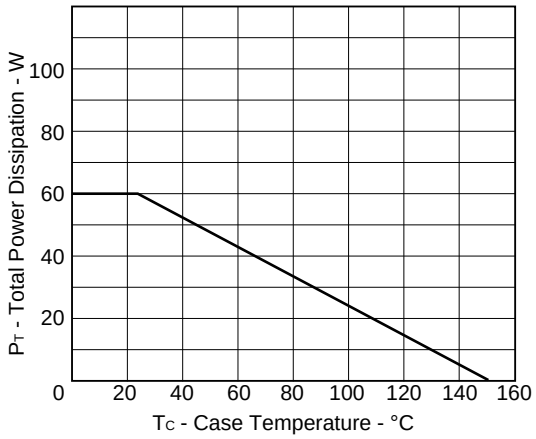
t = 1 μs
Duty Cycle ≤ 1 %

Test Circuit 2 Gate Charge

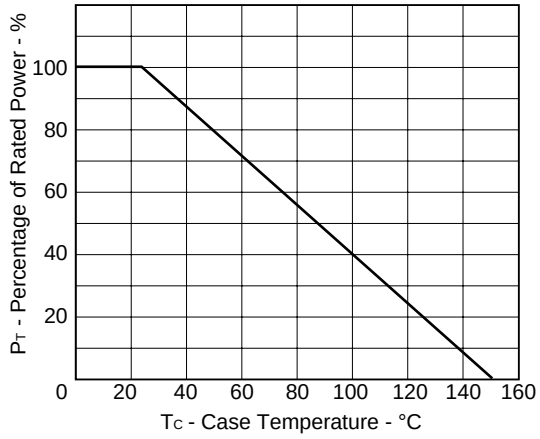


ELECTRICAL CHARACTERISTICS (T_A = 25 °C)

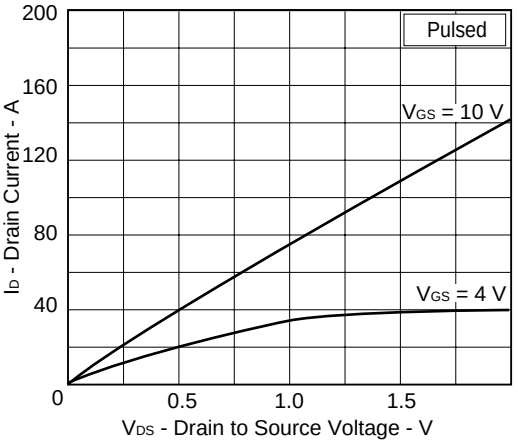
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



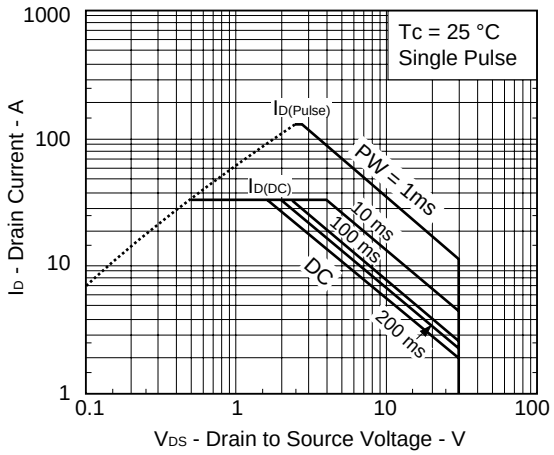
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



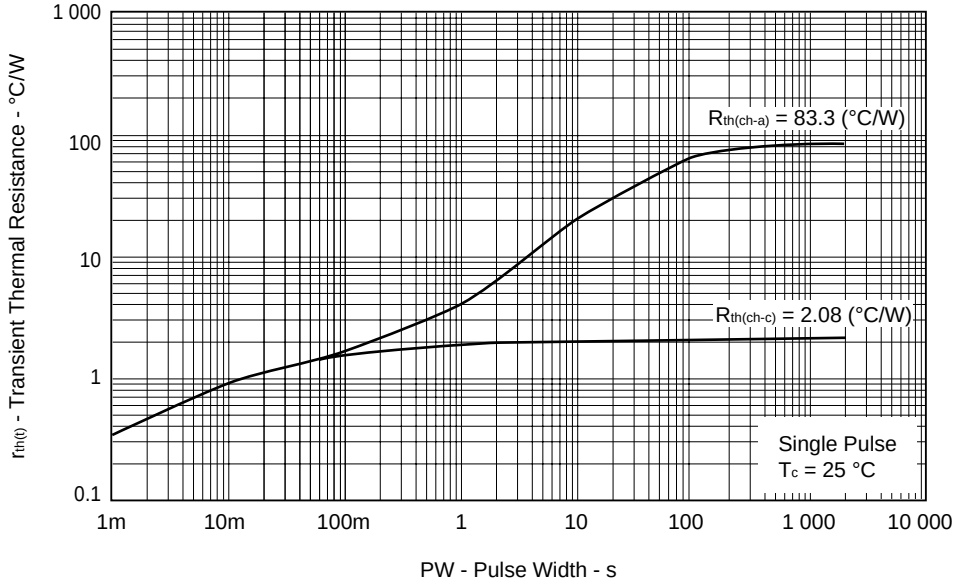
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



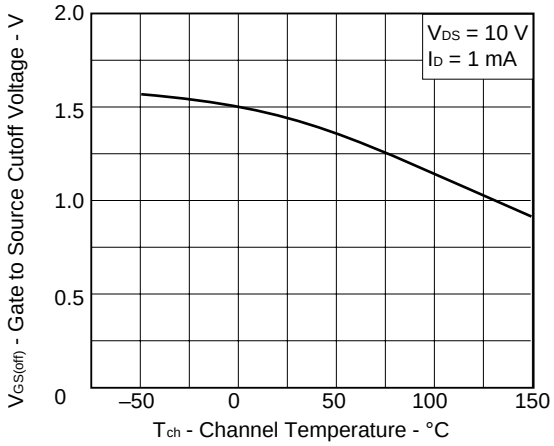
FORWARD BIAS SAFE OPERATING AREA



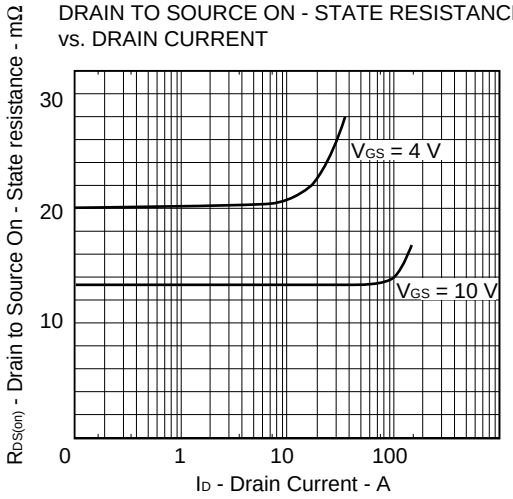
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



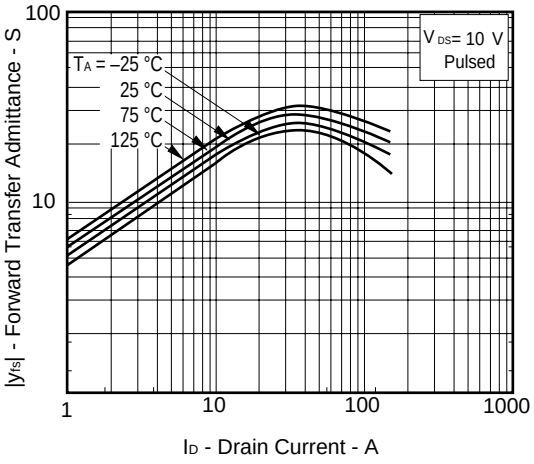
GATE TO SOURCE CUTOFF VOLTAGE vs. CHANNEL TEMPERATURE



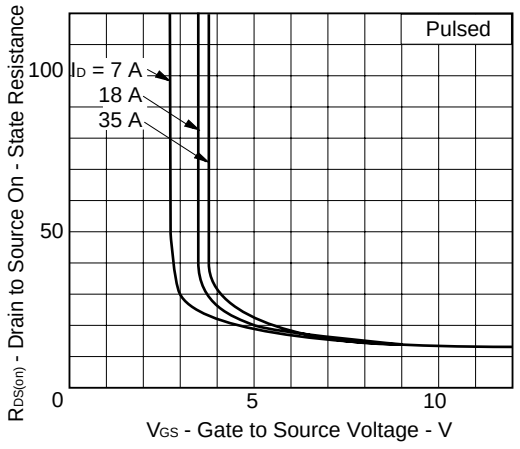
DRAIN TO SOURCE ON - STATE RESISTANCE vs. DRAIN CURRENT



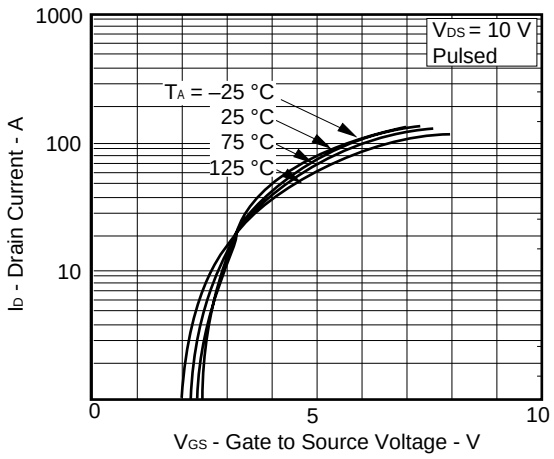
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



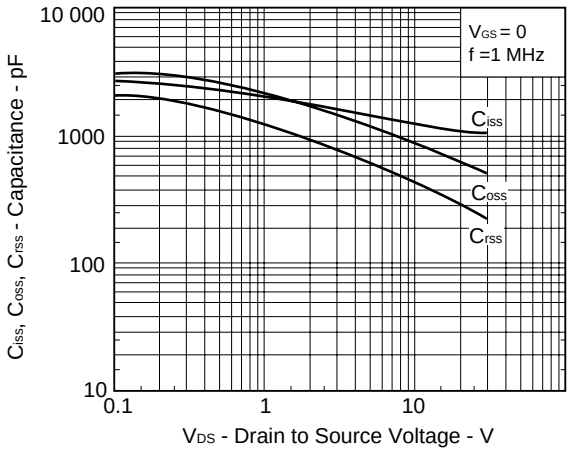
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



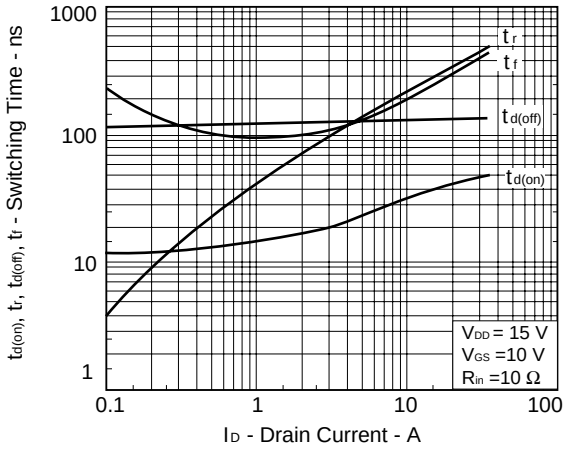
FORWARD TRANSFER CHARACTERISTICS



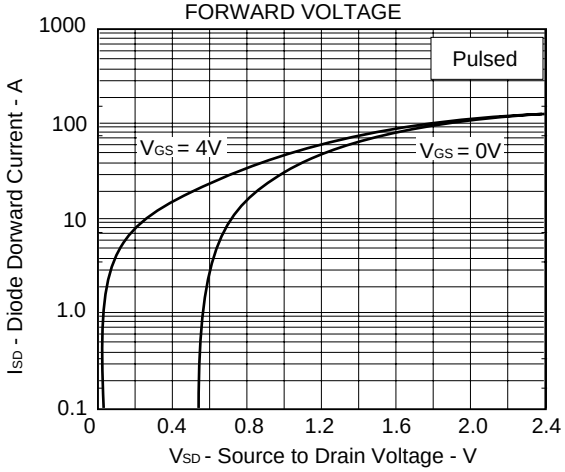
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



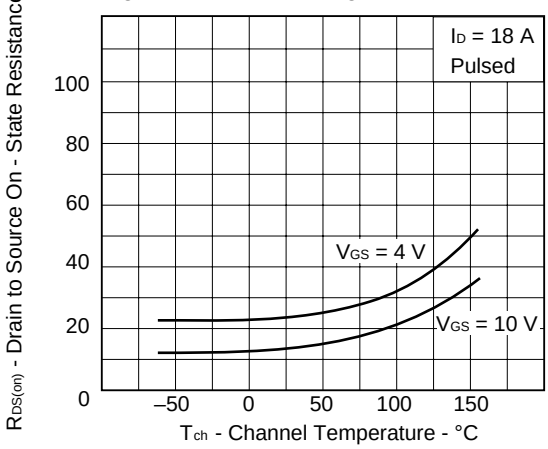
SWITCHING CHARACTERISTICS



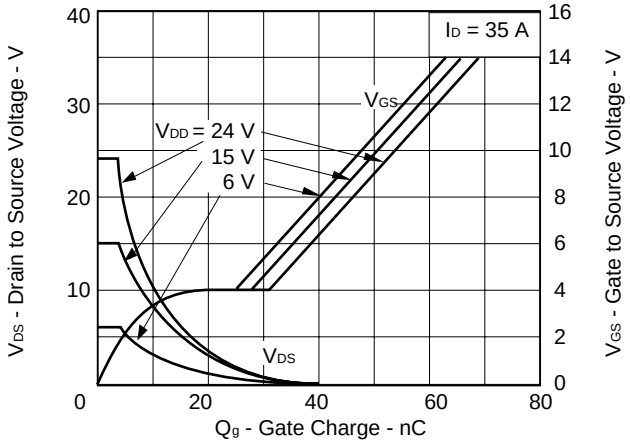
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



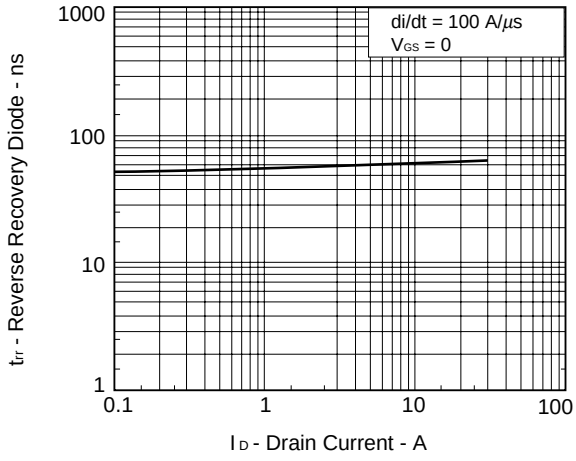
DRAIN TO SOURCE ON-RESISTANCE vs. CHANNEL TEMPERATURE



DYNAMIC INPUT/OUTPUT CHARACTERISTICS



REVERSE RECOVERY TIME vs. DRAIN CURRENT



ELECTRICAL REFERENCE (T_A = 25 °C)

Ducument Name	Ducument No.
NEC semiconductor device reliability/quality control system	C11745E
Quality grade on NEC semiconductor devices	C11531E
Semiconductor device mounting technology manual	C10535E
Semiconductor device package manual	C10943X
Guide to quality assurance for semiconductor devices	MEI-1202
Application circuits using Power MOS FET	TEA-1035
Safe operating area of Power MOS FET	TEA-1037

[MEMO]

M4 96.5