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FJL6920

High Voltage Color Display Horizontal Deflection Output

- High Collector-Base Breakdown Voltage : BV_{CBO} = 1700V
- Low Saturation Voltage : V_{CE}(sat) = 3V (Max.)
- For Color Monitor



1.Base 2.Collector 3.Emitter

NPN Triple Diffused Planar Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Rating	Units V	
V _{CBO}	Collector-Base Voltage	1700		
V _{CEO}	Collector-Emitter Voltage	800	V	
V _{EBO}	Emitter-Base Voltage	6	V	
I _C	Collector Current (DC)	20	A	
I _{CP} *	Collector Current (Pulse)	30	A	
P _C	Collector Dissipation	200	W	
TJ	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	-55 ~ 150	°C	

* Pulse Test: PW=300µs, duty Cycle=2% Pulsed

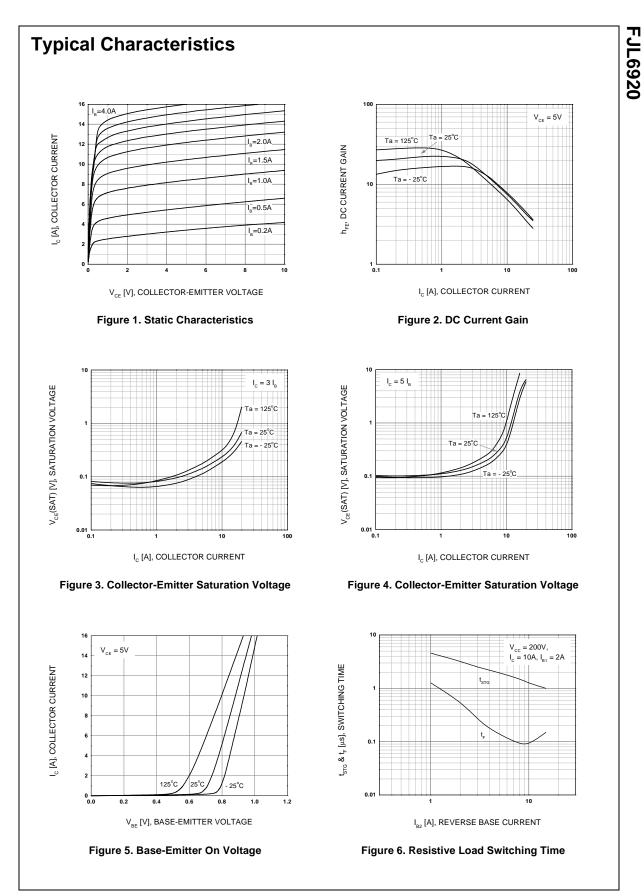
Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
I _{CES}	Collector Cut-off Current	V _{CB} =1400V, R _{BE} =0			1	mA
I _{CBO}	Collector Cut-off Current	V _{CB} =800V, I _E =0			10	μΑ
I _{EBO}	Emitter Cut-off Current	V _{EB} =4V, I _C =0			1	mA
BV _{CBO}	Collector-Base Breakdown Voltage	I _C =500μA, I _E =0	1700			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C =5mA, I _B =0	800			V
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E =500μA, I _C =0	6			V
h _{FE1} h _{FE2}	DC Current Gain	V _{CE} =5V, I _C =1A V _{CE} =5V, I _C =11A	8 5.5		8.5	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =11A, I _B =2.75A			3	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C =11A, I _B =2.75A			1.5	V
t _{STG} *	Storage Time	V_{CC} =200V, I _C =10A, R _L =20 Ω			3	μs
t _F *	Fall Time	I _{B1} =2.0A, I _{B2} = - 4.0A		0.15	0.2	μs

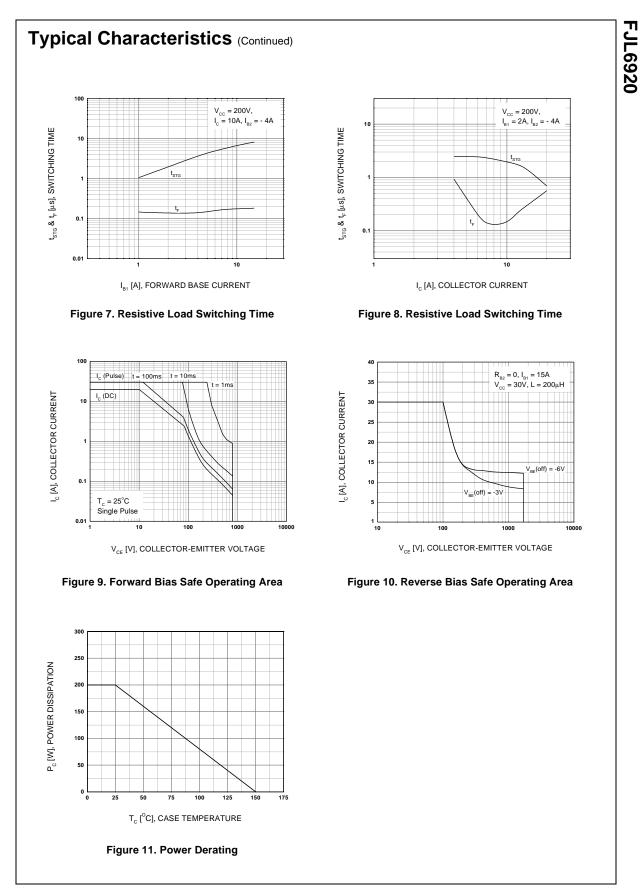
* Pulse Test: PW=20 μ s, duty Cycle=1% Pulsed

Thermal Characteristics $T_C=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Тур	Max	Units
$R_{\theta j C}$	Thermal Resistance, Junction to Case		0.625	°C/W

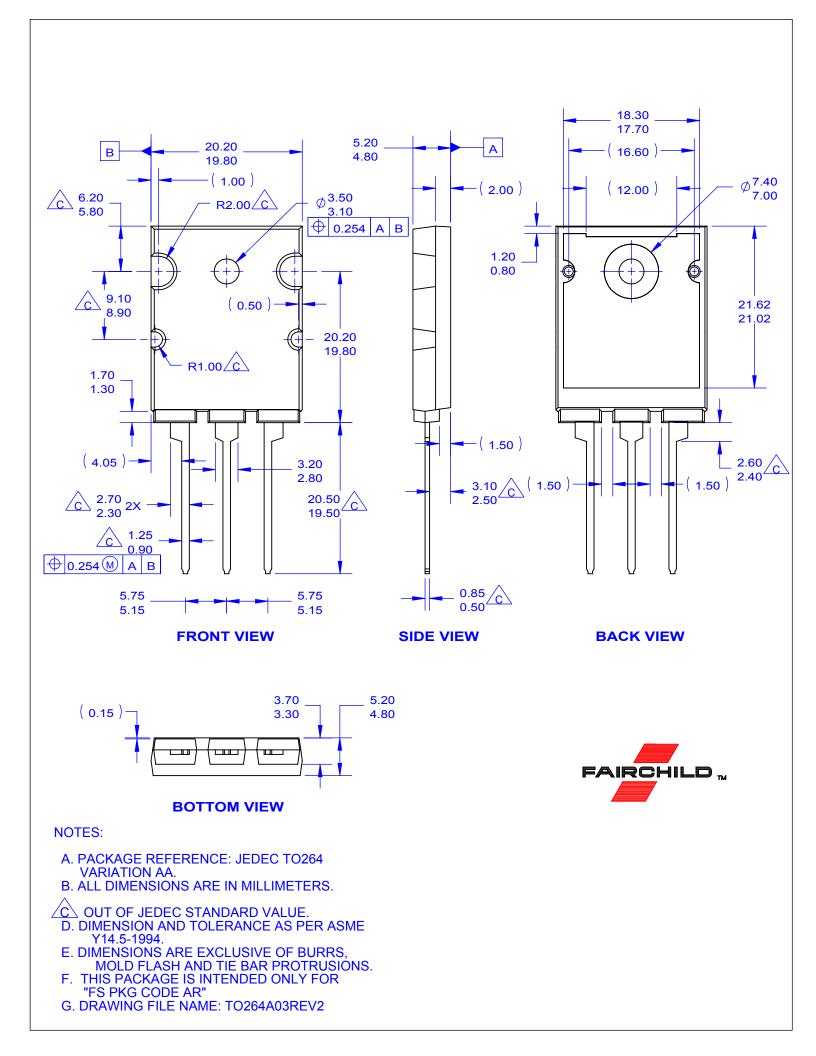


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