AN7348K, AN7348S

Dual Record/Playback Pre-amplifier IC for Double Cassette

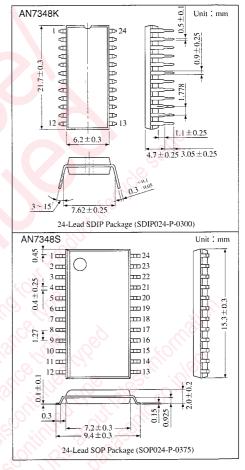
Overview

The AN7348K and AN7348S are dual channel record/playback pre-amplifiers including ALC function developed for double cassette recorder.

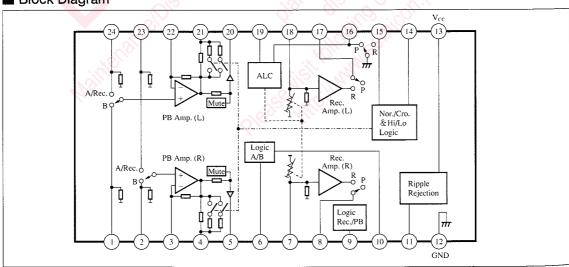
By four pins, Tape A/B, Normal/Chro, Normal/High speed and Record/Playback can be switched.

Features

- PB pre-amplifier supporting Normal/Chrome switching and Normal/High speed switching
- Tape A/B switching, Rec./PB switching supported
- Record pre-amplifier with ALC
- Wide operating supply voltage range: V_{CC}=3.6 to 12V
- FET for head switching not required
- Minimum switching noise
- · Protection from EMI



Block Diagram



■ Pin Description

Pin No.	Pin Name	Pin No.	Pin Name
1	L-ch. PB Amp. Input (B)	13	V _{CC}
2	R-ch. PB Amp. Input (B)	14	Normal/Cro2 Selection
3	R-ch. PB Negative Feedback	15	Hi/Lo Dubbing Selection
4	R-ch. PB Equalizer	16	ALC Time Constant
5	R-ch. PB Output	17	L-ch, REC, Output
6	AB Switching Time Constant	18	L-ch. REC. Input
7	R-ch. REC. Input	19	ALC Low Cut
8	R-ch, REC, Output	20	L-ch. PB Amp. Output
9	REC./PB Selection	21	L-ch. PB Equalizer
10	Tape A/B Selection	22	L-ch. PB Negative Feedback Input
11	Ripple Filter	23	R-ch. PB Amp, Input (A)
12	GND	24	L-ch. PB Amp. Input (A)

■ Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit
Supply Voltage	V _{cc}	13	V
Supply Current	$\mathfrak{I}_{\operatorname{CC}}$	50	mA
Power Dissipation	P _D	562	mW
Operating Ambient Temperature	Topr	-20 ~ +75	C
Storage Temperature	T_{stg}	$-55 \sim +150$	°C (

■ Recommended Operating Range (Ta=25°C)

Parameter	Symbol	Range
Operating Supply voltage Range	V _{cc}	3.6V ~ 12V

■ Electrical Characteristics ($V_{CC}=6V$, $f_{req.}=1kHz$, $R_g=0\Omega$, BPF=400Hz to 30kHz, $Ta=25^{\circ}C\pm2^{\circ}C$)

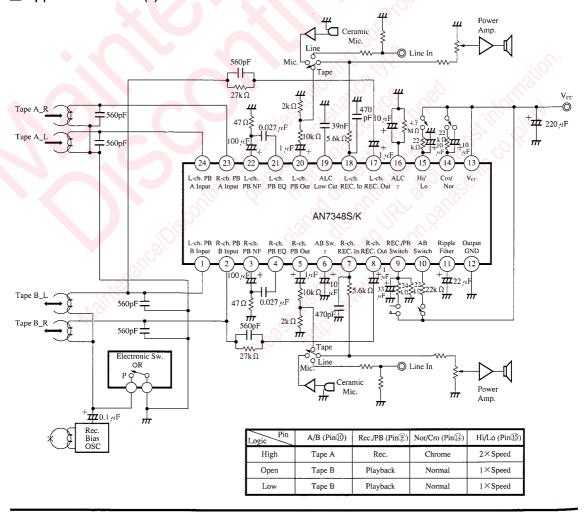
Parameter	Symbol	Condition	min.	typ.	max.	Unit
Consumption Current	I_{CQrec}	V _{in} =0mV	14	20	28	mA
Playback Amp.	-180	is is It	76			
Closed Loop Gain	G_{VC}	V _O =0dBV	41	43	45	dB
Total Harmonics Distortion	THD	BPF, V _o =0dBV	<u> </u>	0.05	0.1	%
Maximum Output Voltage	V _{Omax} .	THD=3%, BPF	1.4	1.7		Vrms
Input Noise	Vni	$R_g = 2.2 k \Omega$, Din Audio $Vni = V_{no}/G_{VC}$	_	1.8	2.5	μV
Open Loop Gain	G_{VO}	V _{in} =89µVrms	75	81		dB
Source Crosstalk	CTs	R_g =2.2k Ω on 1 source Din Audio V_{in} =5.6mVrms	_	-65	-55	dB
Channel Crosstalk	CT_C	$R_g=2.2k\Omega$ on 1 source Din Audio $V_{in}=5.6mVrms$		-65	-55	dB
Channel Balance	CB	V _{in} =5.6mVrms	-1.5	0	1.5	dB
Playback EQ	,					
120 μF/70 μF Relative Gain	⊿G _{VC1}	$V_{in} = 10 \text{mVrms}, f_{req.} = 10 \text{kHz}$ $\Delta G_{VC} = G_{VC} 120 - G_{VC} 70$	4	4.6	5.2	dB
1x/2x Dubbing Relative Gain	⊿G _{VC2}	V_{in} =22mVrms, $f_{req.}$ =10kHz ΔG_{VC} = G_{VC} 1x- G_{VC} 2x	4	4.6	5.2	dB



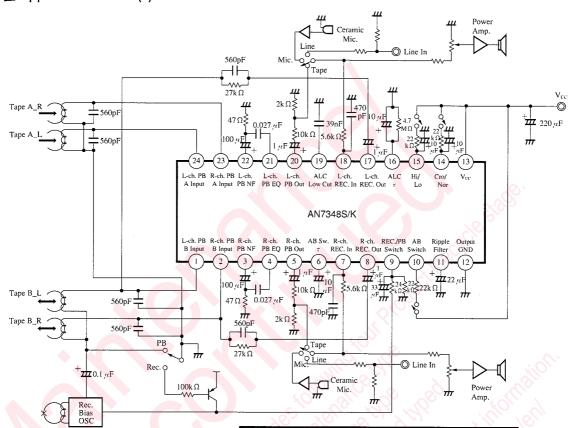
■ Electrical Characteristics (Cont.) ($V_{CC}=6V$, $f_{req.}=1kHz$, $R_g=0\Omega$, BPF=400Hz to 30kHz, $Ta=25\%\pm2\%$)

Parameter	Symbol	Condition	min.	typ.	max.	Unit
Record Amplifier						
Closed Loop Gain	G _{VC3}	V _{in} =12mVrms	37	39	42.5	dB
Total Harmonics Distortion	THD	BPF, Vo=0dBV	-	0.05	0.17	%
Maximum Output Voltage	V _{Omax} .	THD=3%, BPF	1.4	1.8		Vrms
Output Noise	V _{no}	$R_g = 1.0 k \Omega$, Din Audio	—	220	550	μV
Channel Crosstalk	CT_C	$R_g = 1.0 k \Omega$, Din Audio $V_{in} = 8 m V rms$		-66	-55	dB
ALC						-
ALC Voltage	V _{ALC}	Rext=5.6k Ω, Dual i/p V _{in} =12mVrms	0.75	1.0	1.37	Vrms
ALC Range	W _{ALC}	Rext=5.6k Ω , from V_{in} =10mVrms to V_{O} =+3dB	35	51	_	dB
ALC Channel Balance	CB _{ALC}	Rext=5.6k Ω , V_{in} =12mVrms	-2	0	2	dB
Playback Mode				(%)		
PB to Rec. Crosstalk	CT _{R/P}	V _{in} =12mVrms at Rec. i/p (20log V ₀ PB/V ₀ Rec.) BPF		-88	-70	dB

■ Application Circuit (1)



■ Application Circuit (2)



Logic Pin	A/B (Pin10)	Rec./PB (Pin 9)	Nor/Cro (Pin 🎚)	Hi/Lo (Pin(\$)
High	Tape A	Rec.	Chrome	2×Speed
Open	Tape B	Playback	Normal	1×Speed
Low	Tape B	Playback	Normal	1×Speed



■ Pin Description

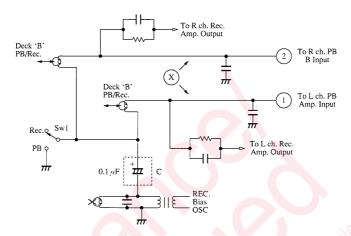
	Description	<u>_</u>	
Pin No.	Pin Name	Description	Equivalent Circuit
1, 2, 23, 24	Tape B Input, Tape A Input	Playback Amplifier Input, Input Impedance = 61k Ω, Pin①, ②, ③ & ④ = 0V (PB Mode), Pin①, ② = 2.1V (Rec. Mode), Pin③, ② = 0V (Rec. Mode)	Pin ² , ³ , ²) 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
3, 22	PB Amp. NF	Playback Amp. Feedback Loop DC=0.7V	1kΩ 112kΩ 1 1 1 1 1 1 1 1 1
4, 21	PB Amp. EQ	PB Equalizer of Nor./Cro. and Hi/Lo Dubbing	T _{100pF} $4.3k\Omega$ $15.3k\Omega$ $2.4k\Omega$ Pin \mathfrak{P}
5, 20	PB Amp. Output	Low Output Impedance	4.7kΩ S Pin ²⁰
6	AB Sw τ	Tape A/B switching time constant determined externally DC=2.8V (Pin High) DC=0.0V (Pin Open)	φ (6) μκΩ ψ (1) μκΩ
7, 18	Rec. In	Rec. Amp. Input Pin Input Impedance = $30k \Omega$	30k Ω 7 Pin 18
8, 17	Rec. Out	Rec. Amp. Output Pin DC=2.6V, Zout=Low (Pin® High) DC=0V, Zout=High (Pin® Open)	27kΩ 51kΩ 8 PinŪ
9	Rec. /PB Sw	Select Rec. or PB Mode Vcc Sw open PB Mode REC Mode	9 5.1kΩ 1000kΩ

■ Pin Description

Pin	Description			
Pin No.	Pin Name	Description	Equivalent Circuit	
10	AB Sw	Select Tape A or Tape B IV Sw open /0V Tape B Tape A	100k Ω 1.5k Ω	
11	Ripple Filter	Connect it with a capacitor to minimize ripple: DC=4.4V (V _{CC} =6V)	9.1kΩ 10kΩ 10kΩ	
12	Output GND	VA 7	1/1/180	
13	Vcc	Typical 6V, Range: 3.5V to 12V	~10g//c	ļ
14	Cro./Nor.	Input to control PB EQ for Chrome or Normal tape		
15	Hi/Lo	Input to control PB EQ for High or Low dubbing Vcc Cro. Cro. Lo Speed Hi Speed Nor. Nor. Lo Speed Hi Speed IV Vcc Pin \$\overline{15}\$	14 Tok Ω 100k Ω Cassett Deck	y le
16	ALC r	Controls the attack and release time of ALC. DC=1.4V→Rec. mode DC=0V→PB mode	$20k\Omega$	
19	ALC Low Cut	ALC comparator circuit reference voltage	27kΩ 22kΩ 19	

■ Operational Description

1) Dubbing mode



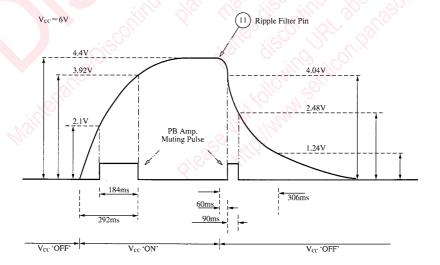
During the dubbing mode, SW1 will be in Rec. position and Point X will be bias at 2.1V internally (V_{CC} =6V). This is to ensure enough dynamic swing for the recording signal. Capacitor C is therefore necessary to couple the DC bias at Point X to Rec. bias oscillator. When in playback made, point X will return to OV and SWI is switched to PB position. Capacitor C should be chosen carefully to avoid degradation of the Rec. bias oscillator signal. The recommended value is 0.1 μ F.

2) ALC Detector

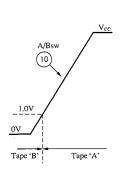
The ALC circuit will be cut off during playback mode. This is achieved internally in the IC by shorting Pin® to ground. By choosing various combinations of R and C at Pin®, the ALC attack and release time can be varied.

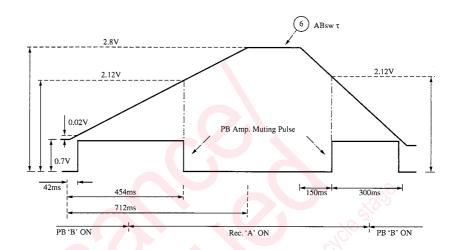
3) Muting Time for Playback Amplifier

a) Power ON/OFF

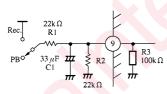


b) A/B Switching Muting

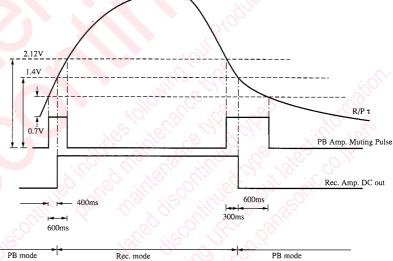




c) R/PB Switching
Muting(A/Bsw='B'mode)

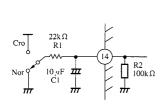


Charging Time ≈ R1*C1 Discharging Time ≈ (R2/R3)*C1

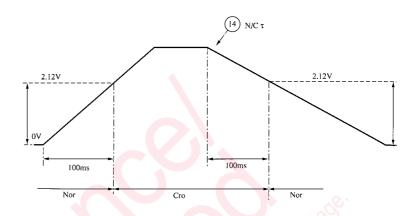




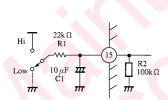
d) Nor/Cro Switching



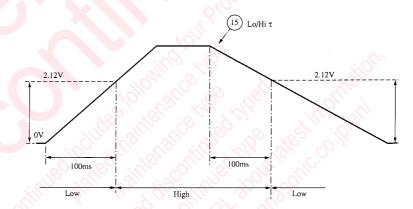
Charging Time \approx R1*C1 Discharging Time \approx (R1/R2)*C1



e) Lo/Hi Dubbing Switching



Charging Time ≈ R1*C1 Discharging Time ≈ (R1/R2)*C1



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