Unit: mm

TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type ( $\pi$ -MOSIV)

# 2SK4014

# DC-DC Converter, Relay Drive and Motor Drive Applications

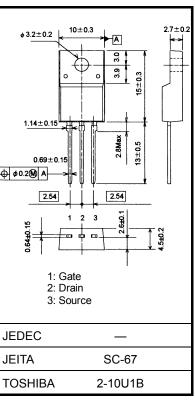
Low drain-source ON-resistance : R<sub>DS (ON)</sub> = 1.6 Ω (typ.)
 High forward transfer admittance : |Y<sub>fs</sub>| = 5.0 S (typ.)

• Low leakage current :  $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 720 \text{ V)}$ 

• Enhancement mode :  $V_{th} = 2.0 \text{ to } 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$ 

### Absolute Maximum Ratings (Ta = 25°C)

Characteri	stic	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	900	V
Drain-gate voltage (RG	<sub>iS</sub> = 20 kΩ)	$V_{DGR}$	900	V
Gate-source voltage		V <sub>GSS</sub>	±30	V
Drain current	DC (Note 1)	ID	6	Α
	Pulse (Note 1)	I <sub>DP</sub>	18	Α
Drain power dissipation	n (Tc = 25°C)	P <sub>D</sub>	45	W
Single-pulse avalanche	e energy (Note 2)	E <sub>AS</sub>	972	mJ
Avalanche current		I <sub>AR</sub>	6	Α
Repetitive avalanche e	nergy (Note 3)	E <sub>AR</sub>	4.5	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature ra	ange	T <sub>stg</sub>	-55 to 150	°C



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

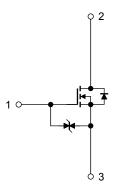
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.78	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 49.5 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 6 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



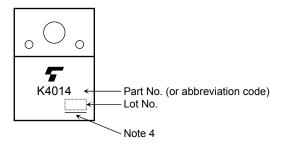
#### **Electrical Characteristics (Ta = 25°C)**

Charac	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	Pakage current $I_{GSS}$ $V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$		_	_	±10	μA	
Gate-source bre	akdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cutoff curr	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 720 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source bre	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	900	_	_	V
Gate threshold v	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source ON	N-resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3 A	_	1.6	2.0	Ω
Forward transfer	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 3 A	2.5	5.0	_	S
Input capacitano	e	C <sub>iss</sub>			1400	_	pF
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	30	_	
Output capacitance		Coss			130	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS} \xrightarrow{0V} \xrightarrow{I_D = 3 \text{ A}} V_{out}$ $50 \Omega  R_{L} = 133 \Omega$ $V_{DD} \approx 400 \text{ V}$	_	25	_	
	Turn-on time	t <sub>on</sub>		_	75	_	ns
	Fall time	t <sub>f</sub>		_	60	_	115
	Turn-off time	t <sub>off</sub>	$V_{DD} \approx 400 \text{ V}$ $Duty \leq 1\%, \ t_W = 10 \mu s$		220	_	
Total gate charge (gate-source plus gate-drain)		Qg			45	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 6 \text{ A}$		25		nC
Gate-drain ("Miller") charge		Q <sub>gd</sub>			20		

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	6	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	-	_	18	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 6 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 6 A, V <sub>GS</sub> = 0 V		1100		ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 100 A / μs	_	10	_	μC

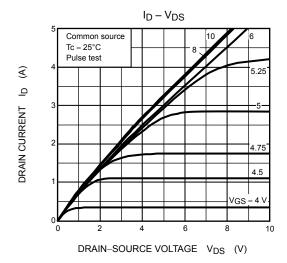
#### Marking

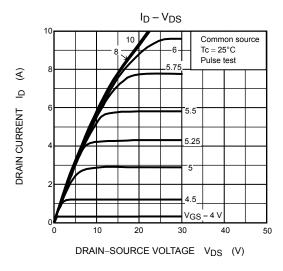


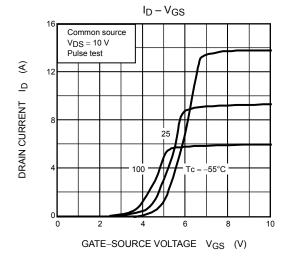
Note 4: A line under a Lot No. identifies the indication of product Labels.

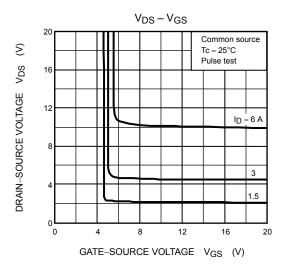
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

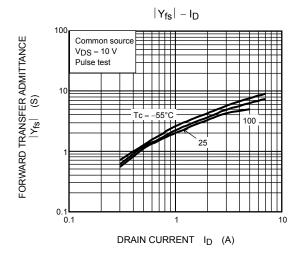
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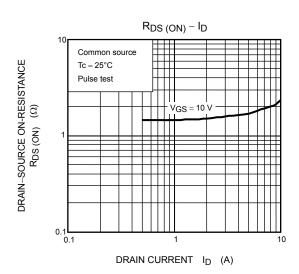




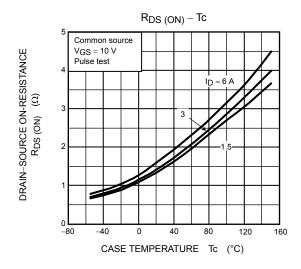


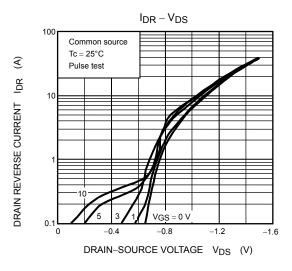


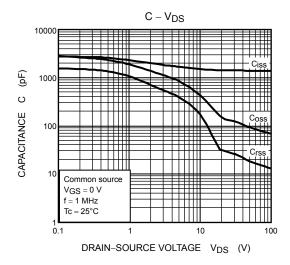


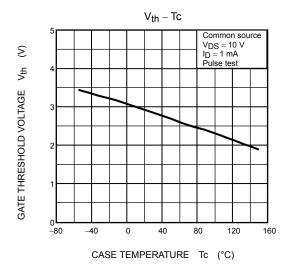


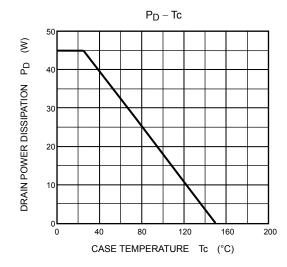
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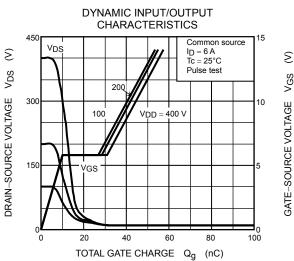




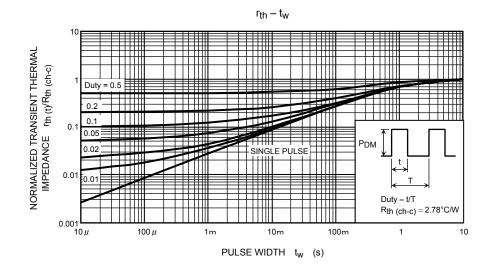


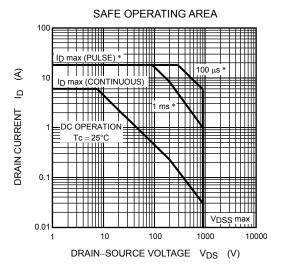


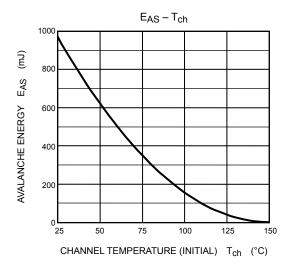


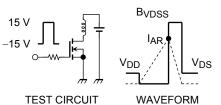


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$$R_G = 25 \Omega$$
  
 $V_{DD} = 90 \text{ V, L} = 49.5 \text{ mH}$   $E_{AS} = \frac{1}{2} \cdot \text{L} \cdot \text{I}^2 \cdot \left(\frac{\text{BVDSS}}{\text{BVDSS} - \text{VDD}}\right)$ 

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