

December 1994

# 54F/74F32 Quad 2-Input OR Gate

# **General Description**

# **Features**

This device contains four independent gates, each of which performs the logic OR function.

■ Guaranteed 4000V minimum ESD protection

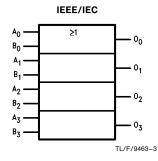
Commercial	Military	Package Number	Package Description	
74F32PC		N14A	14-Lead (0.300" Wide) Molded Dual-In-Line	
	54F32DM (Note 2)	J14A	14-Lead Ceramic Dual-In-Line	
74F32SC (Note 1)		M14A	14-Lead (0.150" Wide) Molded Small Outline, JEDEC	
74F32SJ (Note 1)		M14D	14-Lead (0.300" Wide) Molded Small Outline, EIAJ	
	54F32FM (Note 2)	W14B	14-Lead Cerpack	
	54F32LM (Note 2)	E20A	20-Lead Ceramic Leadless Chip Carrier, Type C	

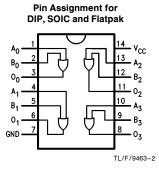
Note 1: Devices also available in 13" reel. Use suffix = SCX and SJX.

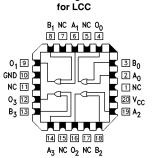
Note 2: Military grade device with environmental and burn-in processing. Use suffix = DMQB, FMQB and LMQB.

# **Logic Symbol**

# **Connection Diagrams**







Pin Assignment

TI /F/9463-1

# **Unit Loading/Fan Out**

		54F/74F				
Pin Names	Description	U.L. HIGH/LOW	Input I <sub>IH</sub> /I <sub>IL</sub> Output I <sub>OH</sub> /I <sub>OL</sub>			
A <sub>n</sub> , B <sub>n</sub> O <sub>n</sub>	Inputs Outputs	1.0/1.0 50/33.3	20 μA/ – 0.6 mA – 1 mA/20 mA			

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#### **Absolute Maximum Ratings** (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

V<sub>CC</sub> Pin Potential to

Voltage Applied to Output

in HIGH State (with  $V_{CC} = 0V$ )

 $\begin{array}{ll} \text{Standard Output} & -0.5 \text{V to V}_{\text{CC}} \\ \text{TRI-STATE} \tiny{\circledR} \text{ Output} & -0.5 \text{V to } +5.5 \text{V} \end{array}$ 

Current Applied to Output in LOW State (Max) twice the rated I<sub>OL</sub> (mA)
ESD Last Passing Voltage (Min) 4000V

**Note 1:** Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

# Recommended Operating Conditions

Free Air Ambient Temperature

Supply Voltage

Military +4.5V to +5.5V Commercial +4.5V to +5.5V

#### **DC Electrical Characteristics**

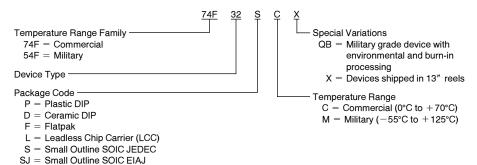
Symbol	Parameter		54F/74F			Units	V <sub>CC</sub>	Conditions	
Symbol			Min	Тур	Max	Onits	VCC	Conditions	
V <sub>IH</sub>	Input HIGH Voltage		2.0			٧		Recognized as a HIGH Signal	
V <sub>IL</sub>	Input LOW Voltage				0.8	V		Recognized as a LOW Signal	
V <sub>CD</sub>	Input Clamp Diode Voltage				-1.2	V	Min	$I_{\text{IN}} = -18  \text{mA}$	
V <sub>OH</sub>	Output HIGH Voltage	54F 10% V <sub>CC</sub> 74F 10% V <sub>CC</sub> 74F 5% V <sub>CC</sub>	2.5 2.5 2.7			V	Min	$I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$	
V <sub>OL</sub>	Output LOW Voltage	54F 10% V <sub>CC</sub> 74F 10% V <sub>CC</sub>			0.5 0.5	٧	Min	$I_{OL} = 20 \text{ mA}$ $I_{OL} = 20 \text{ mA}$	
ІІН	Input HIGH Current	54F 74F			20.0 5.0	μΑ	Max	V <sub>IN</sub> = 2.7V	
I <sub>BVI</sub>	Input HIGH Current Breakdown Test	54F 74F			100 7.0	μΑ	Max	V <sub>IN</sub> = 7.0V	
ICEX	Output HIGH Leakage Current	54F 74F			250 50	μΑ	Max	V <sub>OUT</sub> = V <sub>CC</sub>	
V <sub>ID</sub>	Input Leakage Test	74F	4.75			٧	0.0	I <sub>ID</sub> = 1.9 μA All Other Pins Grounded	
lod	Output Leakage Circuit Current	74F			3.75	μΑ	0.0	V <sub>IOD</sub> = 150 mV All Other Pins Grounded	
I <sub>IL</sub>	Input LOW Current				-0.6	mA	Max	V <sub>IN</sub> = 0.5V	
los	Output Short-Circuit Current		-60		-150	mA	Max	V <sub>OUT</sub> = 0V	
Icch	Power Supply Current			6.1	9.2	mA	Max	V <sub>O</sub> = HIGH	
ICCL	Power Supply Current			10.3	15.5	mA	Max	V <sub>O</sub> = LOW	

#### **AC Electrical Characteristics**

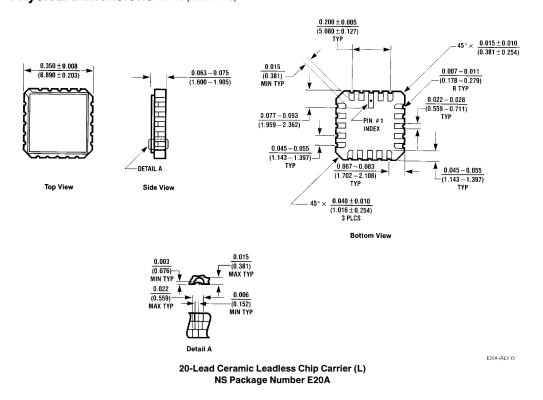
Symbol	Parameter	74F			5-	4F	74F		
		$\begin{aligned} \textbf{T}_{\textbf{A}} &=  +25^{\circ}\textbf{C} \\ \textbf{V}_{\textbf{CC}} &=  +5.0\textbf{V} \\ \textbf{C}_{\textbf{L}} &=  50\textbf{pF} \end{aligned}$			$ extsf{T}_{ extsf{A}},  extsf{V}_{ extsf{CC}} =  extsf{Mil} \  extsf{C}_{ extsf{L}} =  extsf{50 pF}$		T <sub>A</sub> , V <sub>CC</sub> = Com C <sub>L</sub> = 50 pF		Units
		Min	Тур	Max	Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay	3.0	4.2	5.6	3.0	7.5	3.0	6.6	ns
t <sub>PHL</sub>	$A_n$ , $B_n$ to $O_n$	3.0	4.0	5.3	2.5	7.5	3.0	6.3	115

# **Ordering Information**

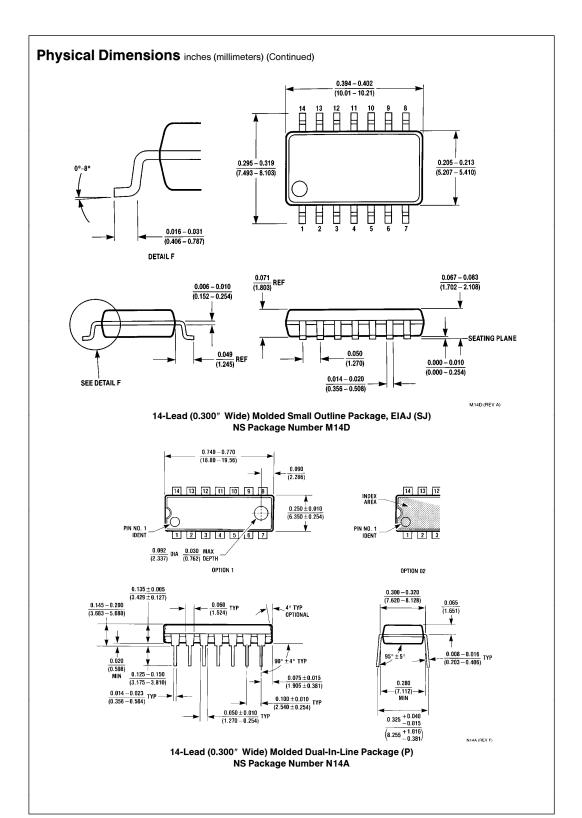
The device number is used to form part of a simplified purchasing code where the package type and temperature range are defined as follows:



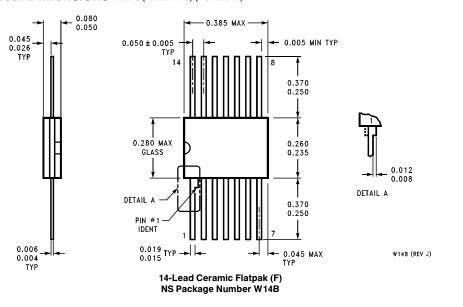
#### Physical Dimensions inches (millimeters)



# Physical Dimensions inches (millimeters) (Continued) 0.785 (19.939) MAX [14] [13] [12] [11] [10] [9] [8] 0.025 (0.635) 0.220-0.310 (5.588-7.874) 1 2 3 4 5 6 7 0.290-0.320 0.005 0.200 GLASS SEALANT (5.080) MAX 0.020-0.060 (D.127) MIN (7.366-8.128) $0.060 \pm 0.005$ (1.524 ±0.127) 0.180 MAX (0.508-1.524) (4.572) 0.008-0.012 10° MAX (0.203-0.305) 0.310-0.410 D.018 ±0.003 0.125-0.200 0.098 (7.874-10.41) (0.457 ±0,076) (3.175-5.080) (2.489) MAX BOTH ENDS 0.100 ±0.010 (2.540 ±0.254) (3.81) MIN J14A (REV G) 14-Lead Ceramic Dual-In-Line Package (D) NS Package Number J14A LEAD NO. 1 IDENT 0.010 (0.254) MAX $\frac{0.150 - 0.157}{(3.810 - 3.988)}$ $\frac{0.053 - 0.069}{(1.346 - 1.753)}$ 8° MAX TYP $\frac{0.004 - 0.010}{(0.102 - 0.254)}$ SEATING PLANE 0.014 (0.356) 0.008 - 0.010 (0.203 - 0.254) TYP ALL LEADS $\frac{0.014-0.020}{(0.356-0.508)}\,\mathrm{TYP}$ 0.016 - 0.050 (0.406 - 1.270) TYP ALL LEADS 0.004 (0.102) ALL LEAD TIPS 0.008 (0.203) TYP 14-Lead (0.150" Wide) Molded Small Outline Package, JEDEC (S) NS Package Number M14A



# Physical Dimensions inches (millimeters) (Continued)



#### LIFE SUPPORT POLICY

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