

Silicon NPN Power Transistors

2SC3927

DESCRIPTION

- With TO-3PN package
- High voltage switching transistor

APPLICATIONS

- For switching regulator and general purpose applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter

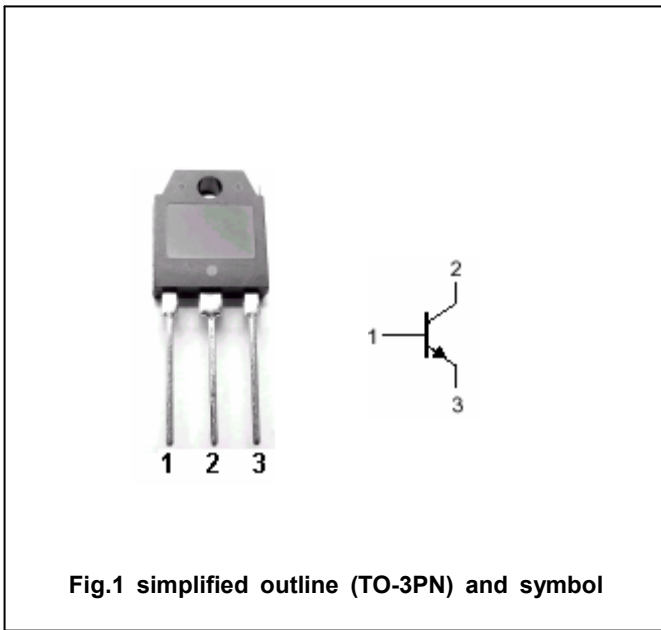


Fig.1 simplified outline (TO-3PN) and symbol

Absolute maximum ratings(Ta=□)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	900	V
V _{CEO}	Collector-emitter voltage	Open base	550	V
V _{EBO}	Emitter-base voltage	Open collector	7	V
I _C	Collector current		10	A
I _{CP}	Collector current-pulse		15	A
I _B	Base current		5	A
P _C	Collector power dissipation	T _C =25□	120	W
T _j	Junction temperature		150	□
T _{stg}	Storage temperature		-55~150	□

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =10mA ; I _B =0	550			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =5A ; I _B =1A			0.5	V
V _{BEsat}	Base-emitter saturation voltage	I _C =5A ; I _B =1A			1.2	V
I _{CBO}	Collector cut-off current	V _{CB} =800V; I _E =0			100	μA
I _{EBO}	Emitter cut-off current	V _{EB} =7V; I _C =0			100	μA
h _{FE}	DC current gain	I _C =5A ; V _{CE} =4V	10		28	
C _{ob}	Output capacitance	I _E =0 ; V _{CB} =10V; f=1MHz		105		pF
f _T	Transition frequency	I _E =1A ; V _{CE} =12V		6		MHz

Switching times

t _{on}	Turn-on time	I _C =5A; I _{B1} =0.75A; I _{B2} =-1.5A R _L =50Ω; V _{CC} =250V			1.0	μs
t _s	Storage time				5.0	μs
t _f	Fall time				0.5	μs

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PACKAGE OUTLINE

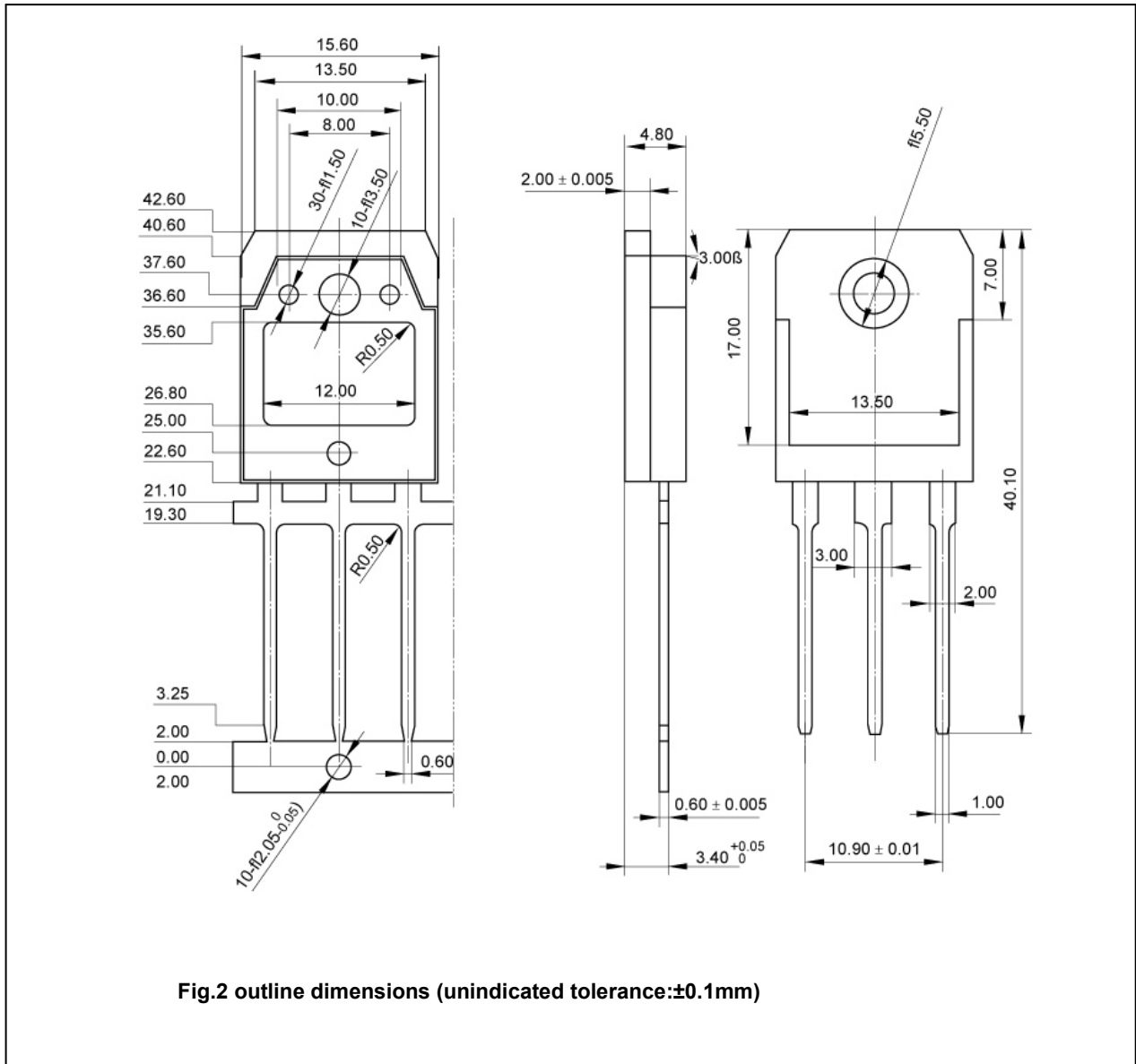


Fig.2 outline dimensions (unindicated tolerance:±0.1mm)

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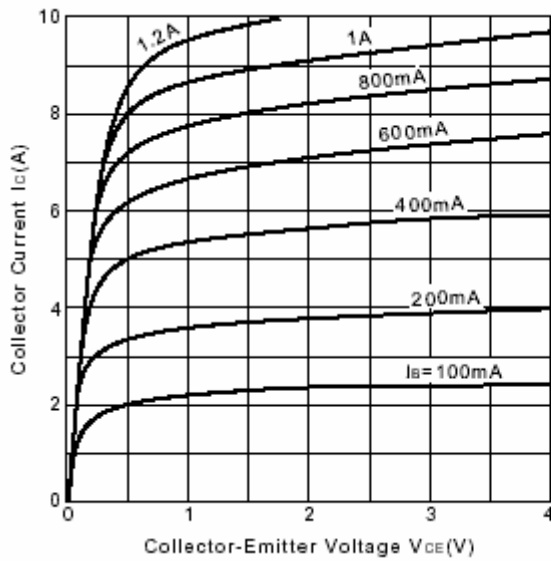


Fig.3 Static Characteristic

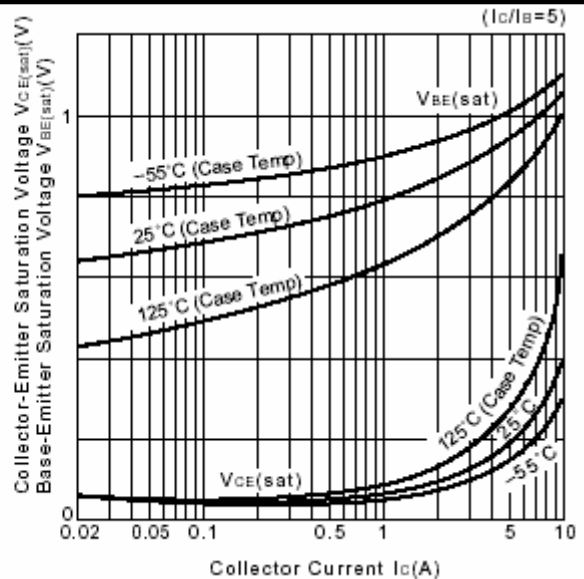


Fig.4 Base-Emitter Saturation Voltage
Collector-Emitter Saturation Voltage

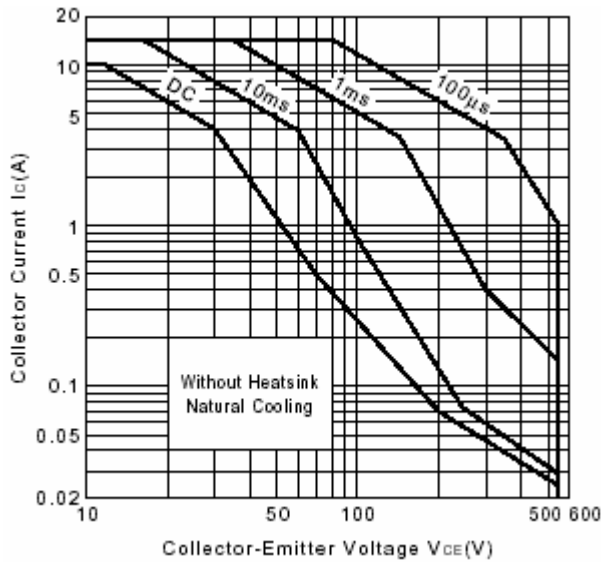


Fig.5 Safe Operating Area

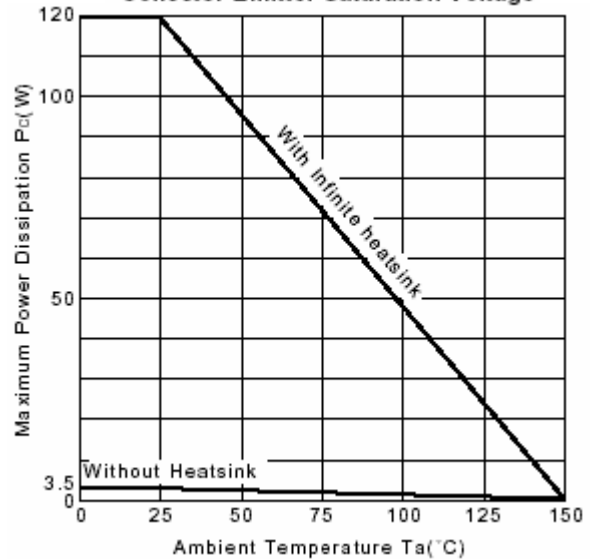


Fig.6 P_c - T_a Derating

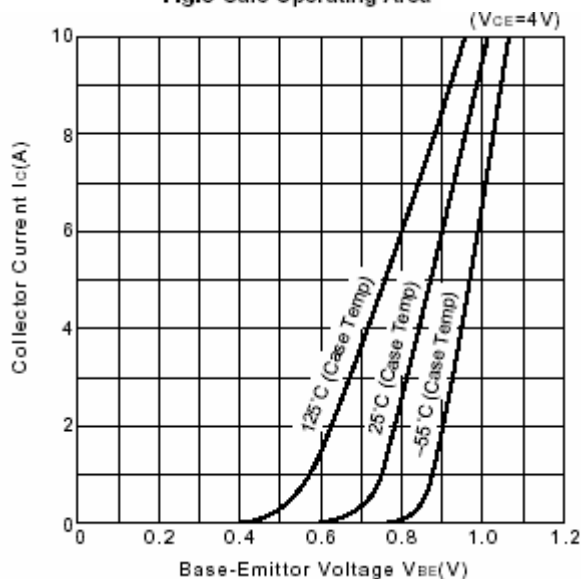


Fig.7 I_c - V_{BE}

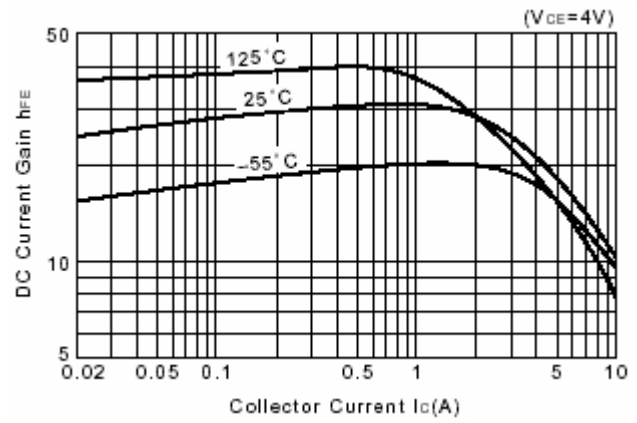


Fig.8 DC current Gain